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Barracuda 50FC Disc Drive
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ST150176FC
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Product Manual, Volume 1
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1.0 Scope

This manual describes Seagate Technology[®], Inc. Barracuda 50FC (Fibre Channel) disc drives.

Barracuda 50FC drives support the Fibre Channel Arbitrated Loop and SCSI Fibre Channel Protocol specifications to the extent described in this manual. The *Fibre Channel Interface Manual* (part number 77767496) describes the general Fibre Channel Arbitrated Loop characteristics of this and other Seagate Fibre Channel drives.

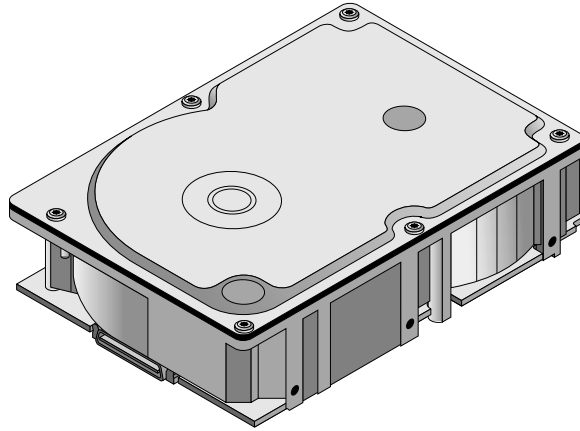


Figure 1. Barracuda 50FC family disc drive

2.0 Applicable standards and reference documentation

The drive has been developed as a system peripheral to the highest standards of design and construction. The drive depends upon its host equipment to provide adequate power and environment in order to achieve optimum performance and compliance with applicable industry and governmental regulations. Special attention must be given in the areas of safety, power distribution, shielding, audible noise control, and temperature regulation. In particular, the drive must be securely mounted in order to guarantee the specified performance characteristics. Mounting by bottom holes must meet the requirements of Section 8.5.

2.1 Standards

The Barracuda 50FC family complies with Seagate standards as noted in the appropriate sections of this manual and the Seagate *Fibre Channel Interface Manual*, part number 77767496.

The Barracuda 50FC disc drive is a UL recognized component per UL1950, CSA certified to CAN/CSA C22.2 No. 950-95, and VDE certified to VDE 0805 and EN60950.

2.1.1 Electromagnetic compatibility

The drive, as delivered, is designed for system integration and installation into a suitable enclosure prior to use. As such the drive is supplied as a subassembly and is not subject to Subpart B of Part 15 of the FCC Rules and Regulations nor the Radio Interference Regulations of the Canadian Department of Communications.

The design characteristics of the drive serve to minimize radiation when installed in an enclosure that provides reasonable shielding. As such, the drive is capable of meeting the Class B limits of the FCC Rules and Regulations of the Canadian Department of Communications when properly packaged. However, it is the user's responsibility to assure that the drive meets the appropriate EMI requirements in their system. Shielded I/O cables may be required if the enclosure does not provide adequate shielding. If the I/O cables are external to the enclosure, shielded cables should be used, with the shields grounded to the enclosure and to the host controller.

2.1.1.1 Electromagnetic susceptibility

As a component assembly, the drive is not required to meet any susceptibility performance requirements. It is the responsibility of those integrating the drive within their systems to perform those tests required and design their system to ensure that equipment operating in the same system as the drive or external to the system does not adversely affect the performance of the drive. See Section 5.1.1 on page 13 and Table 1, DC power requirements, on page 19.

2.1.2 Electromagnetic compliance

Seagate uses an independent laboratory to confirm compliance with the directives/standards for CE Marking and C-Tick Marking. The drive was tested in a representative system for typical applications. The selected system represents the most popular characteristics for test platforms. The system configurations include:

- 486, Pentium, and PowerPC microprocessors
- 3.5-inch floppy disc drive
- Keyboard
- Monitor/display
- Printer
- External modem
- Mouse

Although the test system with this Seagate model complies with the directives/standards, we cannot guarantee that all systems will comply. The computer manufacturer or system integrator shall confirm EMC compliance and provide the appropriate marking for their product.

Electromagnetic compliance for the European Union

If this model has the CE Marking it complies with the European Union requirements of the Electromagnetic Compatibility Directive 89/336/EEC of 03 May 1989 as amended by Directive 92/31/EEC of 28 April 1992 and Directive 93/68/EEC of 22 July 1993.

Australian C-Tick

If this model has the C-Tick Marking it complies with the Australia/New Zealand Standard AS/NZS3548 1995 and meets the Electromagnetic Compatibility (EMC) Framework requirements of Australia's Spectrum Management Agency (SMA).

2.2 Reference documents

Barracuda 50FC Installation Guide

Seagate part number: 83329474

Fibre Channel Interface Manual

Seagate part number: 77767496

ANSI Fibre Channel Documents

X3.230-1994	FC Physical and Signaling Interface (FC-PH)
X3.297.1997	FC-PH-2 Fibre Channel Physical and Signaling Interface-2
X3.303.1998	FC-PH-3 Fibre Channel Physical and Signaling Interface-3
X3.272-1996	FC Arbitrated Loop (FC-AL)
X3.269-1996	Fibre Channel Protocol for SCSI (FCP)
NCITS TR-19	Private Loop SCSI Direct Attach (PLDA)
NCITS TR-20	Fabric Loop Attachment (FC-FLA)
SFF-8045	Specification for 40-pin SCA-2 Connector with Parallel Selection.
SFF-8067	Specification for 40-pin SCA-2 Connector with Bidirectional Enclosure Services Interface.

ANSI Small Computer System Interface (SCSI) Documents

X3.131-1994	(SCSI-2)
X3.270-1996	(SCSI-3) Architecture Model
NCITS 305-199X	(SCSI-3) Enclosure Services
30553-001	Specification for Acoustic Test Requirement and Procedures

In case of conflict between this document and any referenced document, this document takes precedence.

3.0 General description

Barracuda™ 50FC drives combine dual striped magnetoresistive heads, partial response/maximum likelihood (PRML) read channel electronics, embedded servo technology, and a Fibre Channel interface to provide high performance, high capacity data storage for a variety of systems including engineering workstations, network servers, mainframes, and supercomputers.

Barracuda 50FC drives are random access storage devices designed to support the Fibre Channel Arbitrated Loop (FC-AL) and SCSI Fibre Channel Protocol as described in the ANSI specifications, this document, and the *Fibre Channel Interface Manual* (part number 77767496) which describes the general interface characteristics of this drive. ST150176FC drives are classified as intelligent peripherals and provide level 2 conformance (highest level) with the ANSI SCSI-1 standard.

The head and disc assembly (HDA) is sealed at the factory. Air recirculates within the HDA through a non-replaceable filter to maintain a contamination-free HDA environment.

See Figure 2 for an exploded view of the drive. Never disassemble the HDA. This exploded view is for information only. Do not attempt to service items in the sealed enclosure (heads, media, actuator, etc.) as this requires special facilities. The drive contains no parts replaceable by the user and opening the HDA for any reason voids your warranty.

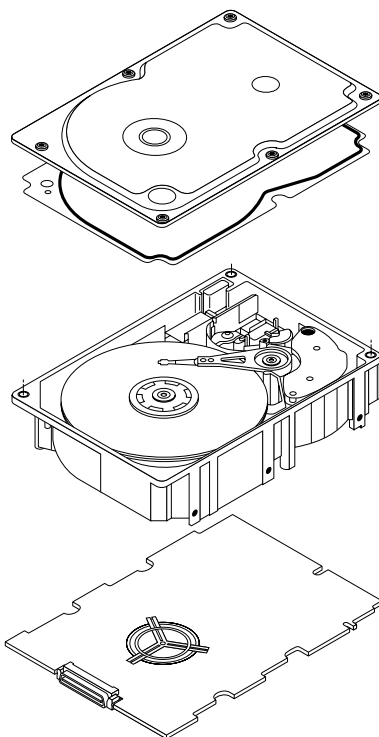


Figure 2. Barracuda 50FC disc drive

Barracuda 50FC drives use a dedicated landing zone at the innermost radius of the media to eliminate the possibility of destroying or degrading data by landing in the data zone. The heads automatically go to the landing zone when power is removed from the drive.

An automatic shipping lock prevents potential damage to the heads and discs that results from movement during shipping and handling. The shipping lock disengages and the head load process begins when power is applied to the drive.

Barracuda 50FC drives decode track location data from the servo data embedded on each surface to eliminate mechanical transducer adjustments and related reliability concerns.

The drives also use a high-performance actuator assembly with a low-inertia, balanced, patented, straight arm design that provides excellent performance with minimal power dissipation.

3.1 Standard features

Barracuda 50FC drives have the following standard features:

- Integrated dual port FC-AL controller
- Concurrent dual port transfers
- Support for FC arbitrated loop, private and public attachment
- Differential copper FC drivers and receivers
- Downloadable firmware using the FC-AL interface
- 128-deep task set (queue)
- Supports up to 32 initiators
- Drive selection ID and configuration options are set on the FC-AL backpanel or through interface commands. Jumpers are not used on the drive.
- Supports SCSI Enclosure Services through the interface connector
- Fibre Channel worldwide name uniquely identifies the drive and each port
- User-selectable logical block size (512 to 4,096 bytes per logical block)
- Selectable frame sizes from 128 to 2,112 bytes
- Industry standard 3.5-inch low profile form factor dimensions
- Programmable logical block reallocation scheme
- Flawed logical block reallocation at format time
- Programmable auto write and read reallocation
- Reed-Solomon error correction code
- Sealed head and disc assembly (HDA)
- No preventive maintenance or adjustments required
- Dedicated head landing zone
- Automatic shipping lock
- Embedded Grey Code track address to eliminate seek errors
- Self-diagnostics performed at power on
- 1:1 interleave
- Zone bit recording (ZBR)
- Vertical, horizontal, or top down mounting
- Dynamic spindle brake
- 1,024 Kbyte data buffer. See Section 4.5
- Embedded servo design
- Reallocation of defects on command (Post Format)
- Fibre Channel interface transports SCSI protocol

3.2 Media description

The media used on the drive has a diameter of approximately 84 mm (approximately 3.4 inches). The aluminum substrate is coated with a thin film magnetic material, overcoated with a proprietary protective layer for improved durability and environmental protection.

3.3 Performance

- Programmable multi-segmentable cache buffer
- 106 Mbytes/sec maximum instantaneous data transfers per port.
- 7,200 RPM spindle; average latency = 4.17 msec
- Command queuing of up to 128 commands
- Background processing of queue
- Supports start and stop commands
- Adaptive seek velocity; improved seek performance

3.4 Reliability

- 1,000,000 hour MTBF
- LSI circuitry

- Balanced low mass rotary voice coil actuator
- Self-Monitoring Analysis and Reporting Technology (S.M.A.R.T.)
- Dithering

3.5 Unformatted and formatted capacities

Formatted capacity depends on the spare reallocation sectors scheme selected, the number of data tracks per sparing zone, and the number of alternate sectors (LBAs) per sparing zone. The following table shows the standard OEM model read capacities data. Total LBAs = read capacity data shown below + 1.

	Formatted	Unformatted
ST150176FC	05D2B03Bh blocks (50 Gbytes with 512 bytes per block)	63.1 Gbytes

Standard OEM models are formatted to 512 bytes per block. You can order other capacities by requesting a different sparing scheme and logical block size.

The sector size is selectable at format time. Users having the necessary equipment may modify the data block size before issuing a format command and obtain different formatted capacities than those listed. ST150176FC drives use a zone sparing scheme. The drive is divided into frequency zones with a variable number of spares in each zone.

3.6 Programmable drive capacity

Using the Mode Select command, the drive can change its capacity to something less than maximum. See the Mode Select (6) parameter list table in the *Fibre Channel Interface Manual*, part number 7767496. A value of zero in the Number of Blocks field indicates that the drive will not change the capacity it is currently formatted to have. A number other than zero and less than the maximum number of LBAs in the Number of Blocks field changes the total drive capacity to the value in the Number of Blocks field. To restore the drive to maximum capacity, enter a value of FFFFFFFF in the Number of Blocks field.

3.7 Factory-installed accessories

OEM standard drives are shipped with the *Barracuda 50FC Installation Guide* (part number 83329474).

3.8 Factory-installed options

You may order the following items which are incorporated at the manufacturing facility during production or packaged before shipping. Some of the options available are (not an exhaustive list of possible options):

- Other capacities can be ordered depending on sparing scheme and sector size requested.
- Single-unit shipping pack. The drive is normally shipped in bulk packaging to provide maximum protection against transit damage. Units shipped individually require additional protection as provided by the single unit shipping pack. Users planning single unit distribution should specify this option.
- The *Barracuda 50FC Installation Guide*, part number 83329474, is usually included with each standard OEM drive shipped, but extra copies may be ordered.

3.9 User-installed accessories

The following accessories are available. All kits may be installed in the field.

- Evaluation kit, part number 73473641.
This kit provides an adapter card ("T-card") to allow cable connections for two FC ports and DC power. Two twin axial cables, 6 feet in length, are included for the input and output connections to the FC interface.
- Single-unit shipping pack.

4.0 Performance characteristics

This section provides detailed information concerning performance-related characteristics and features of Barracuda 50FC drives.

4.1 Internal drive characteristics

		ST150176FC
Drive capacity	50.07	Gbytes (formatted, rounded off value)
Read/write data heads	22	
Bytes per track	189.28	Kbytes (average, rounded off values)
Bytes per surface	2,276	Mbytes (unformatted, rounded off value)
Tracks per surface (total)	12,024	Tracks (user accessible)
Tracks per inch	12,905	TPI
Peak bits per inch	252	KBPI
Internal data rate	153-264	Mbits/sec (variable with zone)
Disc rotation speed	7,184	rpm (\pm 0.5%)
Avg rotational latency	4.17	msec

4.2 Seek performance characteristics

See Section 9.5, "FC-AL physical interface" on page 48 and the *Fibre Channel Interface Manual* (part number 77767496) for additional timing details.

4.2.1 Access time

		Including controller overhead (without disconnect)^{1, 2} (msec)	
		Read	Write
Average	Typical ^{3,4}	7.6	8.4
Single track	Typical ^{3,4}	0.9	1.2
Full stroke	Typical ^{3,4}	16	17

1. Execution time measured from receipt of the FCP Command to the FCP Response.
2. Assumes no errors and no sector has been relocated.
3. Typical access times are measured under nominal conditions of temperature, voltage, and horizontal orientation as measured on a representative sample of drives.
4. Access time = controller overhead + average seek time.
Access to data = controller overhead + average seek time + latency time.

4.2.2 Format command execution time for \geq 512-byte sectors

		ST150176FC
Maximum (with verify)	120 minutes	
Maximum (without verify)	70 minutes	

4.2.3 General performance characteristics

		ST150176FC
Minimum sector interleave		1 to 1
Data buffer to/from disc media (one 512-byte logical block)*		
Minimum		19.1 MBytes/sec
Average		27.8 MBytes/sec
Maximum		33.0 MBytes/sec
Fibre Channel Interface maximum instantaneous transfer rate		106 Mbytes/sec* per port (dual port = 212 Mbytes/sec*)

Logical block sizes

Default is 512-byte data blocks

Variable 512 to 4,096 bytes per sector in $n + 4$ intervals (n = number of bytes per sector).

Read/write consecutive sectors on a track	Yes
Flaw reallocation performance impact (for flaws reallocated at format time using the spare sectors per sparing zone reallocation scheme.)	Negligible
Overhead time for head switch in sequential mode	1.2 msec
Overhead time for one track cylinder switch in sequential mode	1.2 msec (typical)
Average rotational latency	4.17 msec

*Assumes no errors and no relocated logical blocks. Rate measured from the start of the first logical block transfer to or from the host.

4.3 Start/stop time

If the Motor Start option is disabled, the drive becomes ready within 35 seconds after DC power is applied. If a recoverable error condition is detected during the start sequence, the drive executes a recovery procedure and the time to become ready may exceed 35 seconds. Stop time is less than 20 seconds (maximum) from removal of DC power.

If the Motor Start option is enabled, the internal controller accepts the commands listed in the *Fibre Channel Interface Manual* less than 3 seconds after DC power has been applied. After the Motor Start command has been received, the drive becomes ready for normal operations within 25 seconds (excluding the error recovery procedure). The Motor Start command can also be used to command the drive to stop the spindle.

There is no power control switch on the drive.

4.4 Prefetch/multi-segmented cache control

The drive provides a prefetch (read look-ahead) and multi-segmented cache control algorithms that in many cases can enhance system performance. Cache refers to the drive buffer storage space when it is used in cache operations. To select this feature, the host sends the Mode Select command with the proper values in the applicable bytes in page 08h. Prefetch and cache operations are independent features from the standpoint that each is enabled and disabled independently using the Mode Select command; however, in actual operation, the prefetch feature overlaps cache operation somewhat as described in sections 4.5.1 and 4.5.2.

All default cache and prefetch mode parameter values (Mode Page 08h) for standard OEM versions of this drive family are given in Table 16.

4.5 Cache operation

Note. Refer to the *Fibre Channel Interface Manual* for more detail concerning the cache bits.

Of the 1,024 kbytes physical buffer space in the drive, 840 kbytes (3,600 kbytes of the 4,096 kbytes on units with this option) can be used as a cache. The buffer can be divided into logical segments (using Mode Select Page 08h, byte 13) from which data is read and to which data is written.

The drive keeps track of the logical block addresses of the data stored in each segment of the buffer. If the cache is enabled (see RCD bit in the *Fibre Channel Interface Manual*), data requested by the host with a read command is retrieved from the buffer, if possible, before any disc access is initiated. If cache operation is not enabled, the buffer (still segmented with the required number of segments) is still used, but only as circular buffer segments during disc medium read operations (disregarding Prefetch operation for the moment). That is, the drive does not check in the buffer segments for the requested read data, but goes directly to the medium to retrieve it. The retrieved data merely passes through some buffer segment on the way to the host. On a cache miss, all data transfers to the host are in accordance with buffer-full ratio rules. On a cache hit, the drive ignores

the buffer-full ratio rules. See the explanation provided with the information about Mode Page 02h (disconnect/reconnect control) in the *Fibre Channel Interface Manual*.

The following is a simplified description of the prefetch/cache operation:

Case A—read command is received and the first logical block is already in the cache:

1. Drive transfers to the initiator the first logical block requested plus all subsequent contiguous logical blocks that are already in the cache. This data may be in multiple segments.
2. When a requested logical block is reached that is not in any cache segment, the drive fetches it and any remaining requested logical block addresses from the disc and puts them in a segment of the cache. The drive transfers the remaining requested logical blocks from the cache to the host in accordance with the Mode Select Disconnect/Reconnect parameters, page 02h.
3. If the prefetch feature is enabled, refer to section 4.5.2 for operation from this point.

Case B—A Read command requests data, and the first logical block is not in any segment of the cache:

1. The drive fetches the requested logical blocks from the disc and transfers them into a segment, and then from there to the host in accordance with the Mode Select Disconnect/Reconnect parameters, page 02h.
2. If the prefetch feature is enabled, refer to section 4.5.2 for operation from this point.

During a prefetch, the drive crosses a cylinder boundary to fetch data only if the Discontinuity (DISC) bit is set to 1 in bit 4 of byte 2 of the Mode Select parameters page 08h. Default is one for bit 4.

Each cache segment is actually a self-contained circular buffer whose length is an integer number of logical blocks. The wrap-around capability of the individual segments greatly enhances the cache's overall performance, allowing a wide range of user-selectable configurations. The drive supports operation of any integer number of segments from 1 to 16. Divide the 840 Kbytes (3,600 kbytes on units with the 4,096 kbytes option) in the buffer by the number of segments to get the segment size. Default is 3 segments.

Note. The size of each segment is not reported by Mode Sense command page 08h, bytes 14 and 15. The value 0XFFFF is always reported regardless of the actual size of the segment. Sending a size specification using the Mode Select command (bytes 14 and 15) does not set up a new segment size. If the STRICT bit in Mode page 00h (byte 2, bit 1) is set to one, the drive responds as it does for any attempt to change an unchangeable parameter.

4.5.1 Caching write data

Write caching is a write operation by the drive that makes use of a drive buffer storage area where the data to be written to the medium is stored while the drive performs the Write command.

If read caching is enabled (RCD=0), then data written to the medium is retained in the cache to be made available for future read cache hits. The same buffer space and segmentation is used as set up for read functions. The buffer segmentation scheme is set up or changed independently, having nothing to do with the state of RCD. When a write command is issued, if RCD=0, the cache is first checked to see if any logical blocks that are to be written are already stored in the cache from a previous read or write command. If there are, the respective cache segments are cleared. The new data is cached for subsequent Read commands.

If the number of write data logical blocks exceed the size of the segment being written into, when the end of the segment is reached, the data is written into the beginning of the same cache segment, overwriting the data that was written there at the beginning of the operation; however, the drive does not overwrite data that has not yet been written to the medium.

If write caching is enabled (WCE=1), then the drive may return Good status on a write command after the data has been transferred into the cache, but before the data has been written to the medium. If an error occurs while writing the data to the medium, and Good status has already been returned, a deferred error will be generated.

The Synchronize Cache command may be used to force the drive to write all cached write data to the medium. Upon completion of a Synchronize Cache command, all data received from previous write commands will have been written to the medium.

Table 16 shows the mode default settings for the drive.

4.5.2 Prefetch operation

If the Prefetch feature is enabled, data in contiguous logical blocks on the disc immediately beyond that which was requested by a Read command are retrieved and stored in the buffer for immediate transfer from the buffer to the host on subsequent Read commands that request those logical blocks (this is true even if cache operation is disabled). Though the prefetch operation uses the buffer as a cache, finding the requested data in the buffer is a prefetch hit, not a cache operation hit.

To enable Prefetch, use Mode Select page 08h, byte 12, bit 5 (Disable Read Ahead - DRA bit). DRA bit = 0 enables prefetch.

Since data that is prefetched replaces data already in some buffer segments, the host can limit the amount of prefetch data to optimize system performance. The Max Prefetch field (bytes 8 and 9) limits the amount of prefetch. The drive does not use the Prefetch Ceiling field (bytes 10 and 11).

During a prefetch operation, the drive crosses a cylinder boundary to fetch more data only if Mode parameters page 08h, byte 2, bit 4 is set to 1 (Discontinuity--DISC bit).

When prefetch (read look-ahead) is enabled (enabled by DRA = 0), it operates under the control of ARLA (Adaptive Read Look-Ahead). If the host uses software interleave, ARLA enables prefetch of contiguous blocks from the disc when it senses that a prefetch hit will likely occur, even if two consecutive read operations were not for physically contiguous blocks of data (e.g. "software interleave"). ARLA disables prefetch when it decides that a prefetch hit will not likely occur. If the host is not using software interleave, and if two sequential read operations are not for contiguous blocks of data, ARLA disables prefetch, but as long as sequential read operations request contiguous blocks of data, ARLA keeps prefetch enabled.

5.0 Reliability specifications

The following reliability specifications assume correct host and drive operational interface, including all interface timings, power supply voltages, environmental requirements and drive mounting constraints

Seek error rate:	Less than 10 errors in 10^8 seeks
Read Error Rates ¹	
Recovered Data	Less than 10 errors in 10^{12} bits transferred (OEM default settings)
Unrecovered Data	Less than 1 sector in 10^{15} bits transferred
Miscorrected Data	Less than 1 sector in 10^{21} bits transferred
Interface error rate:	Less than 1 error in 10^{12} bits transferred with minimum receive eye. Less than 1 error in 10^{14} bits transferred with typical receive eye. See Section 9.6.4, "Differential PECL input." on page 57
MTBF:	1,000,000 hours
Service life:	5 years
Preventive maintenance:	None required

1. Error rate specified with automatic retries and data correction with ECC enabled and all flaws reallocated.

5.1 Error rates

The error rates stated in this manual assume the following:

- The drive is operated in accordance with this manual using DC power as defined in paragraph 6.2, "DC power requirements."
- The drive has been formatted with the FC-AL Format command.
- Errors caused by media defects or host system failures are excluded from error rate computations.
- Assume random data.

5.1.0.1 Interface errors

An interface error is defined as a failure of the receiver on a port to recover the data as transmitted by the device port connected to the receiver. The error may be detected as a running disparity error, illegal code, loss of word sync, or CRC error. The total error rate for a loop of devices is the sum of the individual device error rates.

5.1.1 Environmental interference

When evaluating systems operation under conditions of electromagnetic interference (EMI), the performance of the drive within the system is considered acceptable if the drive does not generate an unrecoverable condition.

An unrecoverable error, or condition, is defined as one that:

- is not detected and corrected by the drive itself
- is not detected from the error or fault status provided through the drive or FC-AL interface
- is not recovered by normal drive or system recovery procedures without operator intervention

5.1.2 Write errors

Write errors can occur as a result of media defects, environmental interference, or equipment malfunction. Therefore, write errors are not predictable as a function of the number of bits passed.

If an unrecoverable write error occurs because of an equipment malfunction in the drive, the error is classified as a failure affecting MTBF. Unrecoverable write errors are those that cannot be corrected within two attempts at writing the record with a read verify after each attempt (excluding media defects).

5.1.3 Seek errors

A seek error is defined as a failure of the drive to position the heads to the addressed track. There must not be more than ten recoverable seek errors in 10^8 physical seek operations. After detecting an initial seek error, the drive automatically performs an error recovery process. If the error recovery process fails, a seek positioning

error (15h) is reported with a Medium error (3h) or Hardware error (4h) reported in the Sense Key. This is an unrecoverable seek error. Unrecoverable seek errors are classified as failures for MTBF calculations. Refer to the *Fibre Channel Interface Manual*, part number 77767496, for Request Sense information.

5.2 Reliability and service

You can enhance the reliability of Barracuda 50FC disc drives by ensuring that the drive receives adequate cooling. Section 6.0 provides temperature measurements and section 8.4 provides cooling and air flow information which you can use to enhance the service life of the drive.

5.2.1 Mean time between failure (MTBF)

The production disc drive achieves an MTBF of 1,000,000 hours when operated in an environment that ensures the case temperatures specified in Section 6.4.1 are not exceeded. Short-term excursions up to the specification limits of the operating environment will not affect MTBF performance. Operating the drive at case temperatures above these values will adversely affect the drive's ability to meet specifications. See Section 6.4, "Environmental limits".

The MTBF target is specified as device power-on hours (POH) for all drives in service per failure.

The following expression defines MTBF:

$$\text{MTBF per measurement period} = \frac{\text{Estimated power-on operating hours in the period}}{\text{Number of drive failures in the period}}$$

Estimated power-on operating hours means power-on hours per disc drive times the total number of disc drives in service. Each disc drive must have accumulated at least nine months of operation. Data is calculated on a rolling average base for a minimum period of six months.

MTBF is based on the following assumptions:

- 8,760 power-on hours per year
- 250 average on/off cycles per year
- Operating at nominal voltages
- System provides adequate cooling to ensure the case temperatures specified in Section 6.4.1 are not exceeded.

Drive failure means any stoppage or substandard performance caused by drive malfunction.

A S.M.A.R.T. (Self-Monitoring Analysis and Reporting Technology) predictive failure indicates that the drive is deteriorating to an imminent failure and is considered an MTBF hit.

5.2.2 Field failure rate vs time

The expected field failure rate is listed below. Drive utilization will vary. An estimate range of utilization is:

- 720 power-on hours (POH) per month
- 250 on/off cycles per year
- Read/seek/write operation 90% of POH
- System provides adequate cooling to ensure the case temperatures specified in Section 6.4.1 are not exceeded.

Month Parts per million (PPM)

1	2,364
2	1,422
3	1,403
4	1,391
5	1,317
6	1,255
7	1,162
8+	1,025

Failure rate is calculated as follows:

- No system-induced failures counted
- PPM targets include 30% no defect found and handling failures
- Based on 1,000,000 MTBF and 720 POH per month
- Month 1's rate includes a 300 PPM installation failure

5.2.3 Preventive maintenance

No routine scheduled preventive maintenance is required.

5.2.4 Service life

The drive has a useful service life of five years. Depot repair or replacement of major parts is permitted during the lifetime.

5.2.5 Service philosophy

Special equipment is required to repair the drive HDA. To achieve the above service life, repairs must be performed only at a properly equipped and staffed Seagate service and repair facility. Troubleshooting and repair of PCBs in the field is not recommended because of the extensive diagnostic equipment required for effective servicing. There are not spare parts available for this drive. The drive warranty is voided if the HDA is opened.

5.2.6 Service tools

No special tools are required for site installation or recommended for site maintenance. Refer to Section 5.2.5. The depot repair philosophy of the drive precludes the necessity for special tools. Field repair of the drive is not practical because users cannot purchase individual parts for the drive.

5.2.7 Hot plugging the drive

Inserting and removing the drive on the FC-AL will interrupt loop operation. The interruption occurs when the receiver of the next device in the loop must synchronize to a different input signal. FC error detection mechanisms, character sync, running disparity, word sync, and CRC are able to detect any error. Recovery is initiated based on the type of error.

The disc drive defaults to the FC-AL Monitoring state, Pass-through state, when it is powered-on by switching the power or hot plugged. The control line to an optional port bypass circuit (external to the drive), defaults to the Enable Bypass state. If the bypass circuit is present, the next device in the loop will continue to receive the output of the previous device to the newly inserted device. If the bypass circuit is not present, loop operation is temporarily disrupted until the next device starts receiving the output from the newly inserted device and regains synchronization to the new input.

The Pass-through state is disabled while the drive performs self test of the FC interface. The control line for an external port bypass circuit remains in the Enable Bypass state while self test is running. If the bypass circuit is present, loop operation may continue. If the bypass circuit is not present, loop operation will be halted while the self test of the FC interface runs.

When the self test completes successfully, the control line to the bypass circuit is disabled and the drive enters the FC-AL Initializing state. The receiver on the next device in the loop must synchronize to output of the newly inserted drive.

If the self-test fails, the control line to the bypass circuit remains in the Enable Bypass state.

Note. It is the responsibility of the systems integrator to assure that no temperature, energy, voltage hazard, or ESD potential hazard is presented during the hot connect/disconnect operation. Discharge the static electricity from the drive carrier prior to inserting it into the system.

Caution. The drive motor must come to a complete stop prior to changing the plane of operation. This time is required to insure data integrity.

5.2.8 S.M.A.R.T.

S.M.A.R.T. is an acronym for Self-Monitoring Analysis and Reporting Technology. This technology is intended to recognize conditions that indicate imminent drive failure and is designed to provide sufficient warning of a failure to allow you to back up the data before an actual failure occurs.

Note. The drive's firmware monitors specific attributes for degradation over time but can't predict instantaneous drive failures.

Each monitored attribute has been selected to monitor a specific set of failure conditions in the operating performance of the drive and the thresholds are optimized to minimize "false" and "failed" predictions.

Controlling S.M.A.R.T.

The operating mode of S.M.A.R.T. is controlled by the DEXCPT and PERF bits on the Informational Exceptions Control mode page (1Ch). Use the DEXCPT bit to enable or disable the S.M.A.R.T. feature. Setting the DEXCPT bit disables all S.M.A.R.T. functions. When enabled, S.M.A.R.T. collects on-line data as the drive performs normal read and write operations. When the PERF bit is set, the drive is considered to be in "On-line Mode Only" and will not perform off-line functions.

You can measure off-line attributes and force the drive to save the data by using the Rezero Unit command. Forcing S.M.A.R.T. resets the timer so that the next scheduled interrupt is in two hours.

You can interrogate the drive through the host to determine the time remaining before the next scheduled measurement and data logging process occurs. To accomplish this, issue a Log Sense command to log page 0x3E. This allows you to control when S.M.A.R.T. interruptions occur. Forcing S.M.A.R.T. with the RTZ command resets the timer.

Performance impact

S.M.A.R.T. attribute data is saved to the disc so that the events that caused a predictive failure can be recreated. The drive measures and saves parameters once every two hours subject to an idle period on the FC-AL bus. The process of measuring off-line attribute data and saving data to the disc is uninterruptable. The maximum on-line only processing delay is summarized below:

Maximum processing delay

	On-line only delay DEXCPT = 0, PERF = 1	Fully-enabled delay DEXCPT = 0, PERF = 0
S.M.A.R.T. delay times	50 milliseconds	50 milliseconds

Reporting control

Reporting is controlled by the MRIE bits in the Informational Exceptions Control mode page (1Ch). Subject to the reporting method, the firmware will issue to the host an 01-5D00 sense code. The error code is preserved through bus resets and power cycles.

Determining rate

S.M.A.R.T. monitors the rate at which errors occur and signals a predictive failure if the rate of degraded errors increases to an unacceptable level. To determine rate, error events are logged and compared to the number of total operations for a given attribute. The interval defines the number of operations over which to measure the rate. The counter that keeps track of the current number of operations is referred to as the Interval Counter.

S.M.A.R.T. measures error rates. All errors for each monitored attribute are recorded. A counter keeps track of the number of errors for the current interval. This counter is referred to as the Failure Counter.

Error rate is the number of errors per operation. The algorithm that S.M.A.R.T. uses to record rates of error is to set thresholds for the number of errors and their interval. If the number of errors exceeds the threshold before the interval expires, the error rate is considered to be unacceptable. If the number of errors does not exceed the threshold before the interval expires, the error rate is considered to be acceptable. In either case, the interval and failure counters are reset and the process starts over.

Predictive failures

S.M.A.R.T. signals predictive failures when the drive is performing unacceptably for a period of time. The firmware keeps a running count of the number of times the error rate for each attribute is unacceptable. To accomplish this, a counter is incremented each time the error rate is unacceptable and decremented (not to exceed zero) whenever the error rate is acceptable. If the counter continually increments such that it reaches the predictive threshold, a predictive failure is signaled. This counter is referred to as the Failure History Counter. There is a separate Failure History Counter for each attribute.

5.2.9 Product warranty

Beginning on the date of shipment to the customer and continuing for a period of five years, Seagate warrants that each product (including components and subassemblies) that fails to function properly under normal use due to defect in materials or workmanship or due to nonconformance to the applicable specifications will be repaired or replaced, at Seagate's option and at no charge to the customer, if returned by customer at customer's expense to Seagate's designated facility in accordance with Seagate's warranty procedure. Seagate will pay for transporting the repair or replacement item to the customer. For more detailed warranty information, refer to the standard terms and conditions of purchase for Seagate products on your purchase documentation.

The remaining warranty for a particular drive can be determined by calling Seagate Customer Service at 1-800-468-3472. You can also determine remaining warranty using the Seagate web site (www.seagate.com). The drive serial number is required to determine remaining warranty information.

Shipping

When transporting or shipping a drive, use only a Seagate-approved container. Keep your original box. Seagate approved containers are easily identified by the Seagate Approved Package label. Shipping a drive in a non-approved container voids the drive warranty.

Seagate repair centers may refuse receipt of components improperly packaged or obviously damaged in transit. Contact your authorized Seagate distributor to purchase additional boxes. Seagate recommends shipping by an air-ride carrier experienced in handling computer equipment.

Product repair and return information

Seagate customer service centers are the only facilities authorized to service Seagate drives. Seagate does not sanction any third-party repair facilities. Any unauthorized repair or tampering with the factory seal voids the warranty.

6.0 Physical/electrical specifications

This section provides information relating to the physical and electrical characteristics of the drive.

6.1 AC power requirements

None.

6.2 DC power requirements

The voltage and current requirements for a single drive are shown below. Values indicated apply at the drive connector.

Table 1: DC power requirements

	Notes	ST150176FC	
		(Amps)	(Amps)
Voltage		+5V	+12V [2]
Regulation	[5]	±5%	±5% [2]
Avg idle current DC \bar{X}	[1] [7]	1.03	0.71
Maximum starting current (peak DC) DC	[3]	1.1	2.66
(peak AC) AC	[3]	1.28	3.35
Delayed motor start (max) DC	[1] [4]	1.07	0.04
Peak operating current:			
Typical DC \bar{X}	[1] [6]	1.1	1.08
Maximum DC	[1]	1.14	1.15
Maximum (peak) DC		1.4	2.6

- [1] Measured with average reading DC ammeter. Instantaneous +12V current peaks will exceed these values. Power supply at nominal voltage.
- [2] For +12 V, a –10% tolerance is allowed during initial spindle start but must return to ±5% before reaching 7,200 RPM. The ±5% must be maintained after the drive signifies that its power-up sequence has been completed and that the drive is able to accept selection by the host initiator.
- [3] See +12V current profile in Figure 3.
- [4] This condition occurs when the Motor Start option is enabled and the drive has not yet received a Start Motor command.
- [5] See paragraph 6.2.1, "Conducted noise immunity." Specified voltage tolerance includes ripple, noise, and transient response.
- [6] Operating condition is defined as random 8 block reads at 130 I/Os per second. Current and power specified at nominal voltages. Increasing +5 volt supply by +5% increases 5 volt current by < 1.3%. Decreasing +12 volt supply by 5% increases +12 volt current by 1.6%.
- [7] During idle, the drive heads are relocated every 60 seconds to a random location within the band from track zero to one-fourth of maximum track.

General DC power requirement notes.

1. Minimum current loading for each supply voltage is not less than 1.4% of the maximum operating current shown.
2. The +5V and +12V supplies should employ separate ground returns.
3. Where power is provided to multiple drives from a common supply, careful consideration for individual drive power requirements should be noted. Where multiple units are powered on simultaneously, the peak starting current must be available to each device.
4. Parameters, other than spindle start, are measured after a 10-minute warm up.

6.2.1 Conducted noise immunity

Noise is specified as a periodic and random distribution of frequencies covering a band from DC to 10 MHz. Maximum allowed noise values given below are peak-to-peak measurements and apply at the drive power connector.

	+5V	+12V (with spindle motor not running)
0 to 100 kHz	150mV	150mV
100 kHz to 10 MHz	100mV	100mV

6.2.2 Power sequencing

The drive does not require power sequencing. The drive protects against inadvertent writing during power-up and down.

6.2.3 Current profiles

Figure 3 identifies the drive +12V current profile and Figure 4 identifies the drive +5V current profile. The current during the various times is as shown:

- T0 Power is applied to the drive.
- T1 Controller self-tests are performed.
- T2 Spindle begins to accelerate under current limiting after performing internal diagnostics.
- T3 Spindle is up to speed and the Head-Arm restraint is unlocked.

Note: All times and currents are typical. See Table 1 for maximum current requirements.

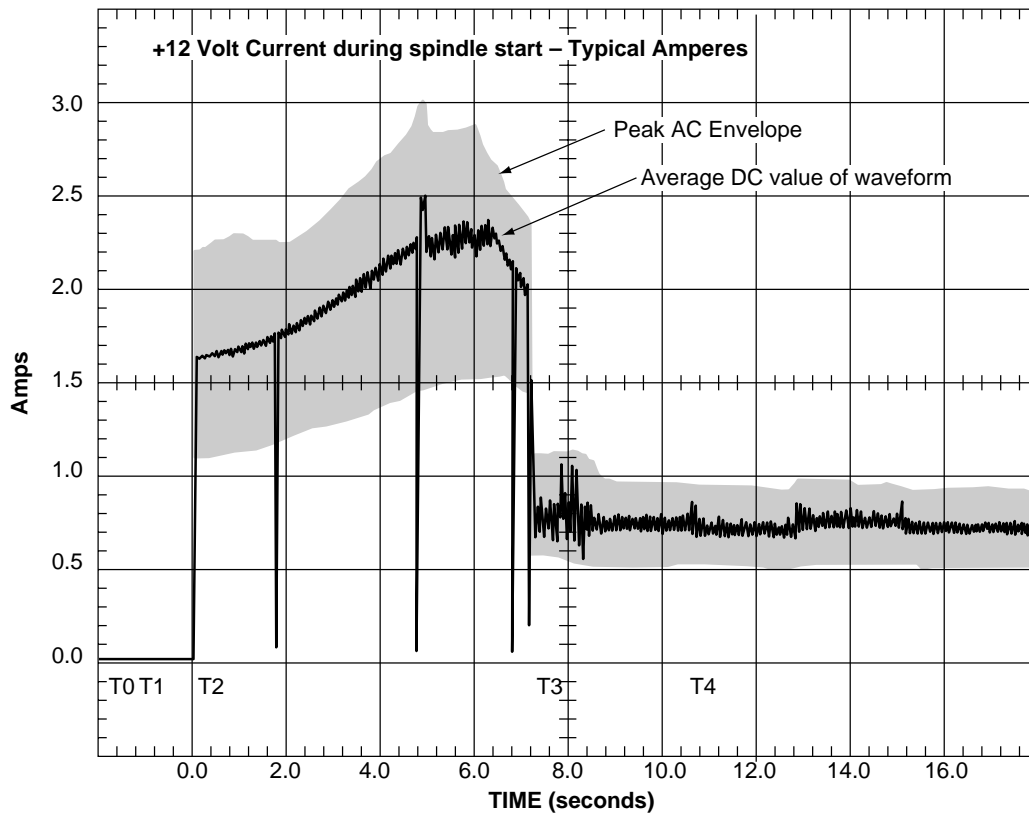


Figure 3. Typical Barracuda 50FC drive +12V current profile

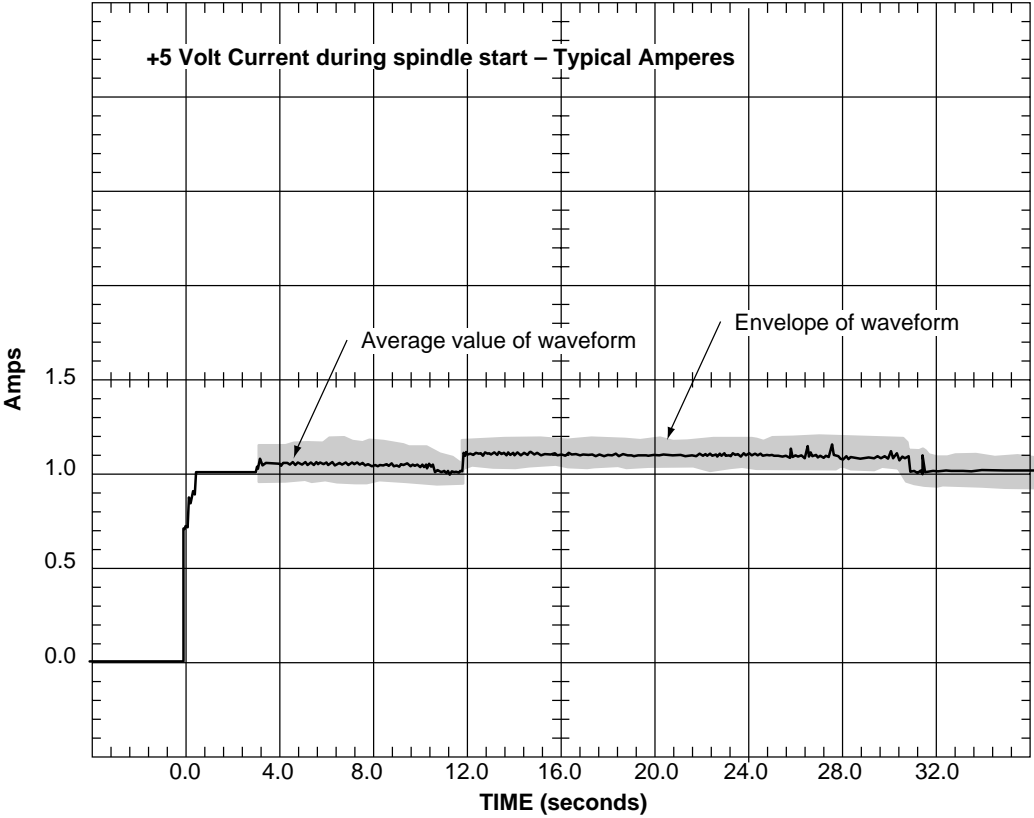


Figure 4. Typical Barracuda 50FC drive +5V current profile

6.3 Power dissipation

Typical power dissipation under idle conditions is 13.67 watts (46.6 BTUs per hour).

To obtain operating power for typical (8 block) random read operations, refer to the following I/O rate curve (see figure 5). Locate the typical I/O rate for a drive in your system on the horizontal axis and read the corresponding +5 volt current, +12 volt current, and the total watts for a drive in your system on the vertical axis. To calculate BTUs per hour, multiply watts by 3.4123.

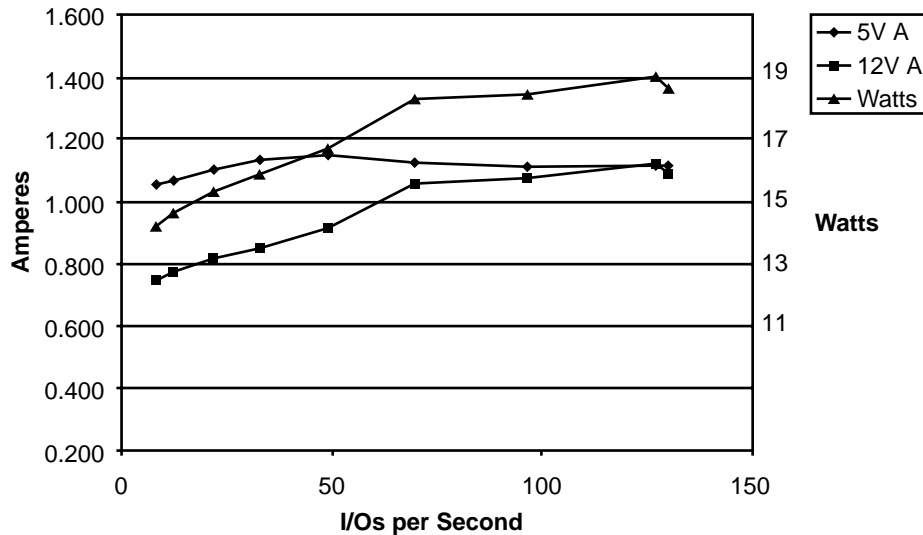


Figure 5. DC current and power vs. input/output operations per second

6.4 Environmental limits

Temperature and humidity values experienced by the drive must be such that condensation does not occur on any drive part. Altitude and atmospheric pressure specifications are referenced to a standard day at 58.7°F (14.8°C). Maximum wet bulb temperature is 82°F (28°C).

6.4.1 Temperature

a. Operating

With cooling designed to maintain the case temperatures of Table 2, the drive meets all specifications over a 41°F to 122°F (5°C to 50°C) drive ambient temperature range with a maximum temperature gradient of 36°F (20°C) per hour. The enclosure for the drive should be designed such that the temperatures at the locations specified in Table 2 are not exceeded. Air flow may be needed to achieve these temperature values (see Section 8.4 and 8.4.1). Operation at case temperatures above these values may adversely affect the drives ability to meet specifications.

The MTBF specification for the drive is based on operating in an environment that ensures that the case temperatures specified in Table 2 are not exceeded. Occasional excursions to drive ambient temperatures of 122°F (50°C) or 41°F (5°C) may occur without impact to specified MTBF. Air flow may be needed to achieve these temperatures (see Section 8.4.1). Continual or sustained operation at case temperatures above these values may degrade MTBF.

To confirm that the required cooling for the electronics and HDA is provided, place the drive in its final mechanical configuration, perform random write/read operations. After the temperatures stabilize, measure the case temperature of the components listed in Table 2 (see note [2]).

The maximum allowable HDA case temperature is 60°C. Operation of the drive at the maximum case temperature is intended for short time periods only. Continuous operation at the elevated temperatures will reduce product reliability.

Table 2: PCBA and HDA temperatures

Items in Figure 6	Maximum allowable case temperature (°C) operating	Maximum allowable case [3] temperatures (°C) to meet MTBF spec.
HDA [2]	60	45
Processor	63	43
A/D Demod	81	61
FC Interface	91	71
Motor Driver	66	46

Notes.

- [1] Section 8.4.1 describes the air-flow patterns used when generating the 1 million hours MTBF guidelines in column 2. Air flow was opposite that shown in Section 8.4.1. Local air velocity was 1.64 m/sec (330 lfm). Inlet air temperature to the drive was 77°F (25°C), plus 9°F (5°C) temperature rise in the test enclosure (86°F/30°C ambient local to the drive).
- [2] Measure HDA temp at point labeled “HDA” on Figure 6.
- [3] PCB mounted integrated circuit case.

b. Non-operating

−40° to 158°F (−40° to 70°C) package ambient with a maximum gradient of 36°F (20°C) per hour. This specification assumes that the drive is packaged in the shipping container designed by Seagate for use with drive.

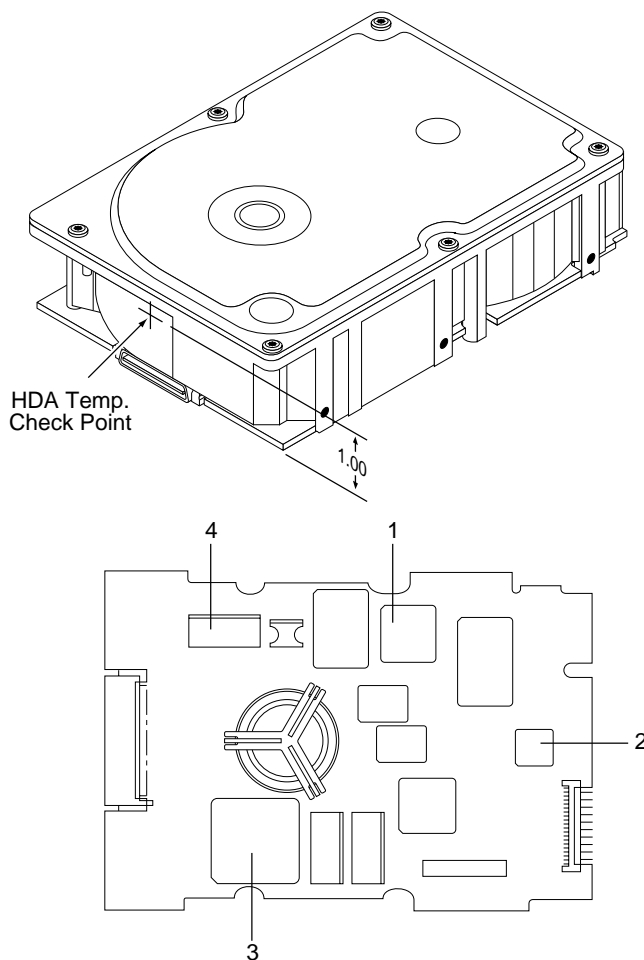


Figure 6. Locations of PCBA components listed in Table 2

6.4.2 Relative humidity

The values below assume that no condensation on the drive occurs.

a. Operating

5% to 95% non-condensing relative humidity with a maximum gradient of 10% per hour.

b. Non-operating

5% to 95% non-condensing relative humidity.

6.4.3 Effective altitude (sea level)

a. Operating

–1,000 to +10,000 feet (–305 to +3,048 meters)

b. Non-operating

–1,000 to +40,000 feet (–305 to +12,210 meters)

6.4.4 Shock and vibration

Shock and vibration limits specified in this document are measured directly on the drive chassis. If the drive is installed in an enclosure to which the stated shock and/or vibration criteria is applied, resonances may occur internally to the enclosure resulting in drive movement in excess of the stated limits. If this situation is apparent, it may be necessary to modify the enclosure to minimize drive movement.

The limits of shock and vibration defined within this document are specified with the drive mounted by any of the four methods shown in Figure 7, and in accordance with the restrictions of Section 8.5. Orientation of the side nearest the LED may be up or down.

6.4.4.1 Shock

a. Operating—normal

The drive, as installed for normal operation, shall operate error free while subjected to intermittent shock not exceeding 5.0 Gs at a maximum duration of 11 msec (half sinewave). Shock may be applied in the X, Y, or Z axis.

b. Operating—abnormal

Equipment, as installed for normal operation, does not incur physical damage while subjected to intermittent shock not exceeding 10 Gs at a maximum duration of 11 msec (half sinewave). Shock occurring at abnormal levels may promote degraded operational performance during the abnormal shock period. Specified operational performance will continue when normal operating shock levels resume. Shock may be applied in the X, Y, or Z axis. Shock is not to be repeated more than two times per second.

c. Non-operating

The limits of non-operating shock shall apply to all conditions of handling and transportation. This includes both isolated drives and integrated drives.

The drive subjected to nonrepetitive shock not exceeding 50 Gs at a maximum duration of 11 msec (half sinewave) shall not exhibit device damage or performance degradation. Shock may be applied in the X, Y, or Z axis.

The drive subjected to nonrepetitive shock not exceeding 150 Gs at a maximum duration of 2 msec (half sinewave) does not exhibit device damage or performance degradation. Shock may be applied in the X, Y, or Z axis.

The drive subjected to nonrepetitive shock not exceeding 45 Gs at a maximum duration of 0.5 msec (half sinewave) does not exhibit device damage or performance degradation. Shock may be applied in the X, Y, or Z axis.

d. Packaged

Disc drives shipped as loose load (not palletized) general freight will be packaged to withstand drops from heights as defined in the table below. For additional details refer to Seagate specifications 30190-001 (under 100 lbs/45 kg) or 30191-001 (over 100 lbs/45 Kg).

Package size	Packaged/product weight	Drop height
<600 cu in (<9,800 cu cm)	Any	60 in (1524 mm)
600-1800 cu in (9,800-19,700 cu cm)	0-20 lb (0 to 9.1 kg)	48 in (1219 mm)
>1800 cu in (>19,700 cu cm)	0-20 lb (0 to 9.1 kg)	42 in (1067 mm)
>600 cu in (>9,800 cu cm)	20-40 lb (9.1 to 18.1 kg)	36 in (914 mm)

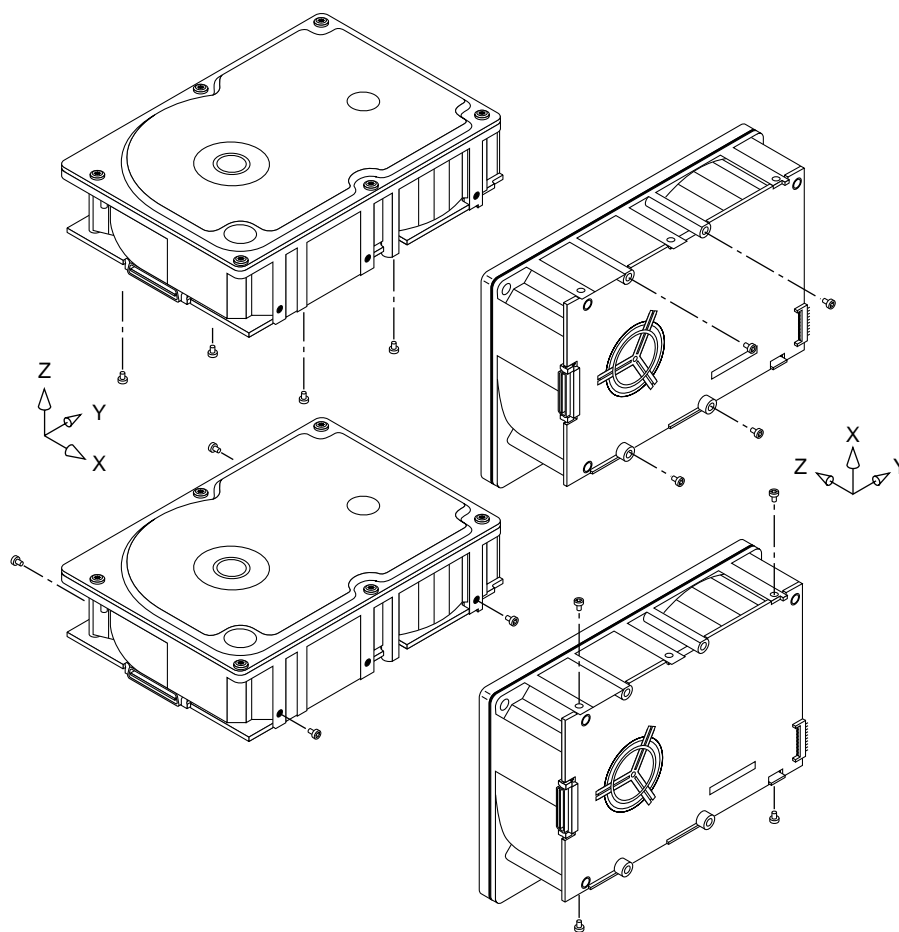


Figure 7. Recommended mounting

6.4.4.2 Vibration

a. Operating - normal

The drive as installed for normal operation, shall comply with the complete specified performance while subjected to continuous vibration not exceeding

5-400 Hz @ 0.5 G

Vibration may be applied in the X, Y, or Z axis.

b. Operating - abnormal

5-400 Hz @ 0.75 G (X, Y, or Z axis) 0 to peak

c. Non-operating

The limits of non-operating vibration shall apply to all conditions of handling and transportation. This includes both isolated drives and integrated drives.

The drive shall not incur physical damage or degraded performance as a result of continuous vibration not exceeding

5-22 Hz @ 0.081 inches (2.05 mm) displacement

22-400 Hz @ 2.00 G

Vibration may be applied in the X, Y, or Z axis.

6.4.5 Air cleanliness

The drive is designed to operate in a typical office environment with minimal environmental control.

6.4.6 Acoustics

Sound power during idle mode shall be 4.0 bels typical when measured to Seagate specification 30553-001.

6.4.7 Electromagnetic susceptibility

See Section 2.1.1.1.

6.5 Mechanical specifications

Refer to Figure 8 for detailed mounting configuration dimensions. See Section 8.5, “Drive mounting.”

Height:	1.62 in	41.1 mm
Width:	4.000 in	101.6 mm
Depth:	5.75 in	146.05 mm
Weight:	2.2 pounds	0.99 kilograms

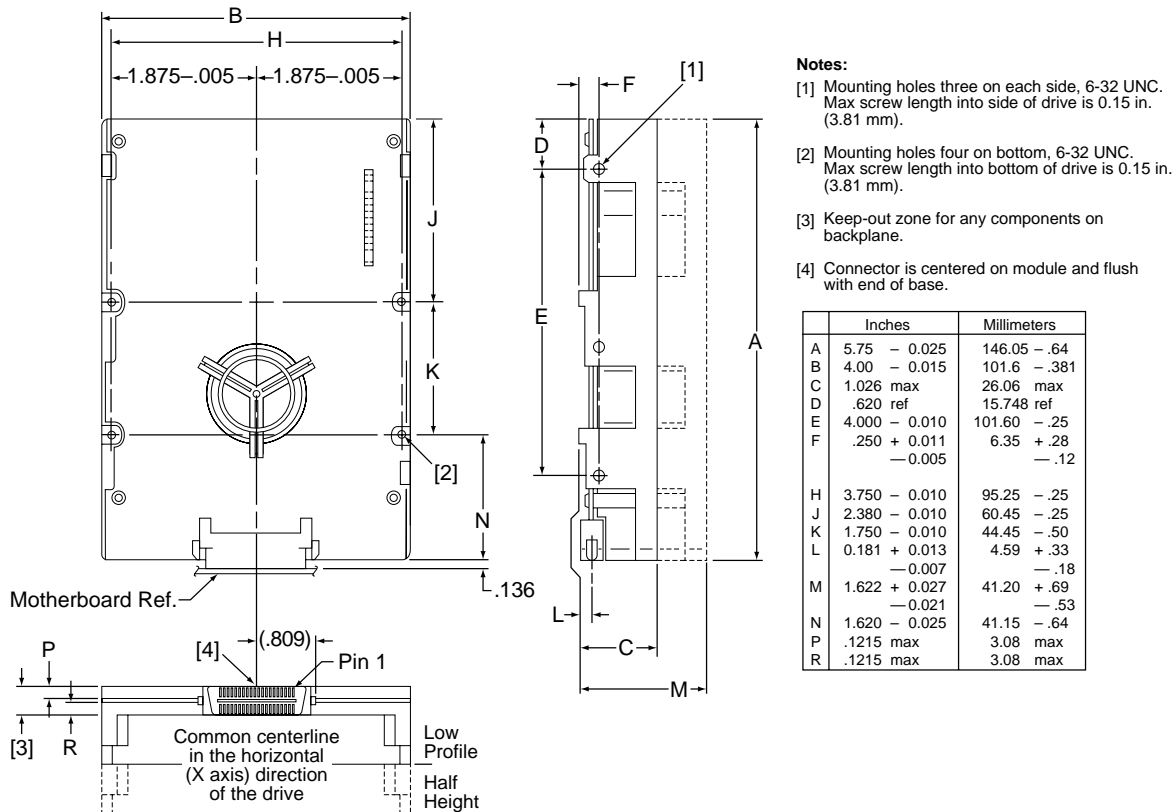


Figure 8. Mounting configuration dimensions

7.0 Defect and error management

The drive, as delivered, complies with this product manual. The read error rates and specified storage capacities are not dependent upon use of defect management routines by the host (initiator).

Defect and error management in the SCSI protocol involves the drive internal defect/error management and FC-AL system error considerations (errors in communications between the initiator and the drive). Tools for use in designing a defect/error management plan are briefly outlined in this section. References to other sections are provided when necessary.

7.1 Drive internal defects/errors

During the initial drive format operation at the factory, media defects are identified, tagged as being unusable, and their locations recorded on the drive primary defects list (referred to as the "P" list and also as the ETF defect list). At factory format time, these known defects are also reallocated, that is, reassigned to a new place on the medium and the location listed in the defects reallocation table. The "P" list is not altered after factory formatting. Locations of defects found and reallocated during error recovery procedures after drive shipment are listed in the "G" list (defects growth list). The "P" and "G" lists may be referenced by the initiator using the Read Defect Data command.

Details of the SCSI commands supported by the drive are described in the *Fibre Channel Interface Manual*. Also, more information on the drive Error Recovery philosophy is presented in the *Fibre Channel Interface Manual*.

7.2 Drive error recovery procedures

When an error occurs during drive operation, the drive, if programmed to do so, performs error recovery procedures to attempt to recover the data. The error recovery procedures used depend on the options previously set in the Error Recovery Parameters mode page. Error recovery and defect management may involve using several SCSI commands described in the *Fibre Channel Interface Manual*. The drive implements selectable error recovery time limits required in video applications.

The error recovery scheme supported by the drive provides a way to control the total error recovery time for the entire command in addition to controlling the recovery level for a single LBA. The total amount of time spent in error recovery for a command can be limited using the Recovery Time Limit bytes in the Error Recovery mode page. The total amount of time spent in error recovery for a single LBA can be limited using the Read Retry Count or Write Retry Count bytes in the Error Recovery mode page.

The drive firmware error recovery algorithms consists of 12 levels for read recoveries and 16 levels for write. Each level may consist of multiple steps, where a step is defined as a recovery function involving a single re-read or re-write attempt. The maximum level used by the drive in LBA recovery is determined by the read and write retry counts.

Table 3 equates the read and write retry count with the maximum possible recovery time for read and write recovery of individual LBAs. The times given do not include time taken to perform reallocations. Reallocations are performed when the ARRE bit (for reads) or AWRE bit (for writes) is one, the RC bit is zero, and the recovery time limit for the command has not yet been met. Time needed to perform reallocation is not counted against the recovery time limit.

When the RC bit is one, reallocations are disabled even if the ARRE or AWRE bits are one. The drive will still perform data recovery actions within the limits defined by the Read Retry Count, Write Retry Count, and Recovery Time Limit parameters. However, the drive does not report any unrecovered errors.

Table 3: Read and write retry count maximum recovery times

Read retry count ¹	Maximum recovery time per LBA (cumulative, msec)	Write retry count ¹	Maximum recovery time per LBA (cumulative, msec)
0	108.7	0	50.2
1	125.4	1	75.2
2	426.4	2	83.6
3	459.8	3	108.7
4	526.7	4	259.2
5	593.6	5 (default)	317.7
6	760.3		
7	877.2		
8	919.0		
9	985.1		
10	2,273.2		
11 (default)	3,223.4		

[1] These values may change at any time. They are presently for general information only.

Setting these retry counts to a value below the default setting could result in degradation of the unrecovered error rate. For example, suppose the read/write recovery page has the RC bit = 0, the read retry count set to 4, and the recovery time limit set to 450. A 4-block read command can take up to 526.68 msec recovery time for each block and a maximum of 450 msec recovery for all four blocks. If either of these limits is reached and a block has not yet been recovered, the command will end with Check Condition status and an unrecoverable read error will be reported.

7.3 FC-AL system errors

Information on the reporting of operational errors or faults across the interface is given in the *Fibre Channel Interface Manual*. The FCP Response returns information to the host about numerous kinds of errors or faults. The Receive Diagnostic Results reports the results of diagnostic operations performed by the drive.

Status returned by the drive to the initiator is described in the *Fibre Channel Interface Manual*. Status reporting plays a role in systems error management and its use in that respect is described in sections where the various commands are discussed.

8.0 Installation

Barracuda 50FC disc drive installation is a plug-and-play process. There are no jumpers, switches, or terminators on the drive. Simply plug the drive into the host's 40-pin Fibre Channel backpanel connector (FC-SCA)—no cables are required. See Section 9.5 for additional information about this connector.

Use the FC-AL interface to select drive ID and all option configurations for devices on the loop.

If multiple devices are on the same FC-AL and physical addresses are used, set the device selection IDs (SEL IDs) on the backpanel so that no two devices have the same selection ID. This is called the hard assigned arbitrated loop physical address (AL_PA). There are 125 AL_PAs available (see Table 22 on page 55). If you set the AL_PA on the backpanel to any value other than 0, the device plugged into the backpanel's SCA connector inherits this AL_PA. In the event you don't successfully assign unique hard addresses (and therefore have duplicate selection IDs assigned to two or more devices), the FC-AL generates a message indicating this condition. If you set the AL_PA on the backpanel to a value of 0, the system issues a unique soft-assigned physical address automatically.

Loop initialization is the process used to verify or obtain an address. The loop initialization process is performed when power is applied to the drive, when a device is added or removed from the Fibre Channel loop, or when a device times out attempting to win arbitration.

- Set all option selections in the connector prior to applying power to the drive. If you change options after applying power to the drive, recycle the drive power to activate the new settings.
- It is not necessary to low-level format this drive. The drive is shipped from the factory low-level formatted in 512-byte logical blocks. You need to reformat the drive only if you want to select a different logical block size.

8.1 Drive ID/option selection

All drive options are made through the interface connector (J1). Table 19 on page 51 provides the pin descriptions for the 40-pin Fibre Channel single connector (J1).

8.2 LED connections

A connector, J6, is provided on the printed circuit board assembly (PCBA) to provide port bypass, drive active, and drive fault LED connections (see Figure 10 on page 32). See Sections 9.5.7, 9.5.8, and 9.5.9 for descriptions of LED functions.

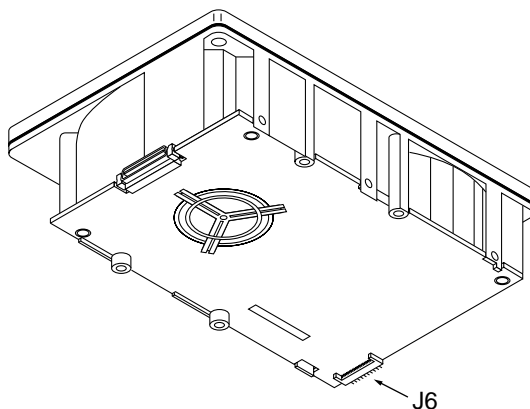
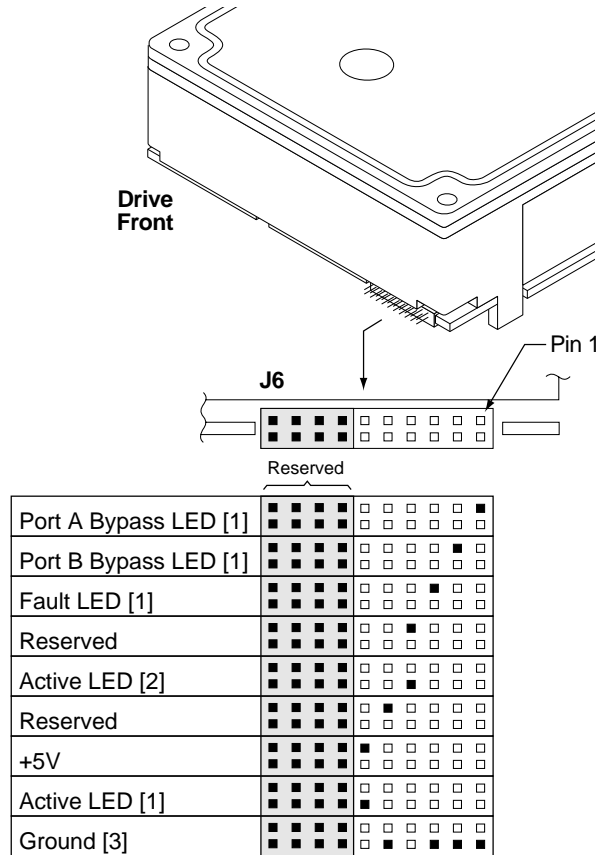


Figure 9. Physical interface



- [1] The drive has a 2.2K ohm resistor in series with this LED driver. Tie the minus side of an external high-efficiency LED (i.e., 2ma) to this pin. Connect the plus side of the LED to +5V.
- [2] An external current-limiting resistor is required when connecting an LED to this pin. The minus side of the resistor/LED combination is connected to this pin. Connect the plus side to +5V.
- [3] Jumper storage location (across pins 2 and 4).

Figure 10. LED indicator connector

8.2.1 J6 connector requirements

Recommended mating connector part number: Berg receptacle, 6-position, Berg part number 690-006.

8.3 Drive orientation

The drive may be mounted in any orientation. All drive performance characterizations, however, have been done with the drive in horizontal (discs level) and vertical (drive on its side) orientations, which are the two preferred mounting orientations.

8.4 Cooling

Cabinet cooling must be designed by the customer so that the ambient temperature immediately surrounding the drive will not exceed temperature conditions specified in Section 6.4.1, "Temperature." Specific consideration should be given to make sure adequate air circulation is present around the printed circuit board (PCB) to meet the requirements of Section 6.4.1, "Temperature."

8.4.1 Air flow

The rack, cabinet, or drawer environment for the drive must provide heat removal from the electronics and head and disc assembly (HDA). You should confirm that adequate heat removal is provided using the temperature measurement guidelines described in Section 6.4.1.

Forced air flow may be required to keep temperatures at or below the specified case temperatures of Table 2, in which case the drive should be oriented, or air flow directed, so that the least amount of air flow resistance is created while providing air flow to the electronics and HDA. Also, the shortest possible path between the air inlet and exit should be chosen to minimize the travel length of air heated by the drive and other heat sources within the rack, cabinet, or drawer environment.

If forced air is determined to be necessary, possible air-flow patterns are shown in Figure 11. The air-flow patterns are created by one or more fans, either forcing or drawing air as shown in the illustrations. Conduction, convection, or other forced air-flow patterns are acceptable as long as the temperature measurement guidelines of Section 6.4.1 are met.

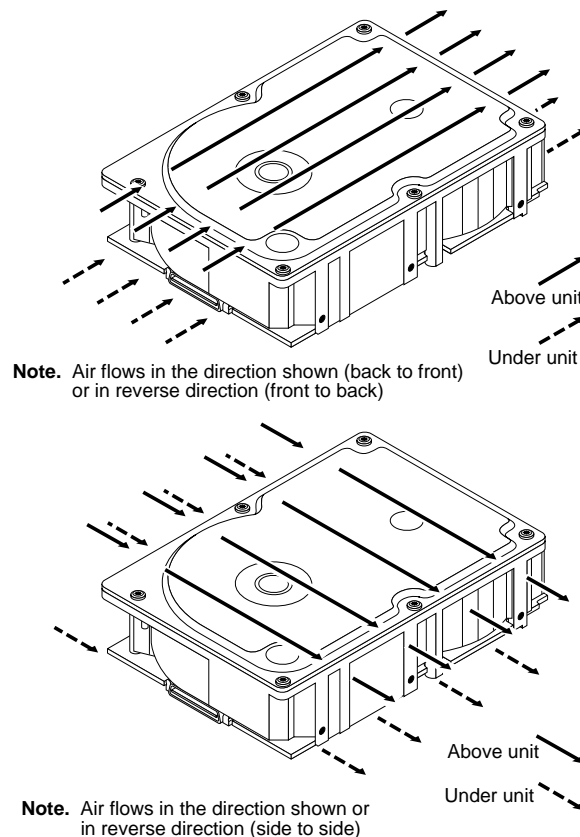


Figure 11. Air flow

8.5 Drive mounting

Mount the drive using the bottom or side mounting holes. If you mount the drive using the bottom holes, ensure that you do not physically distort the drive by attempting to mount it on a stiff, non-flat surface.

The allowable mounting surface stiffness is 80 lb/in (14.0 N/mm). The following equation and paragraph define the allowable mounting surface stiffness:

$$K \times X = F \leq 15\text{lb} = 67\text{N}$$

where K is the mounting surface stiffness (units in lb/in or N/mm) and X is the out-of-plane surface distortion (units in inches or millimeters). The out-of-plane distortion (X) is determined by defining a plane with three of the four mounting points fixed and evaluating the out-of-plane deflection of the fourth mounting point when a known force (F) is applied to the fourth point.

Note. Before mounting the drive in any kind of 3.5-inch to 5.25-inch adapter frame, verify with Seagate Technology that the drive can meet the shock and vibration specifications given herein while mounted in such an adapter frame. Adapter frames that are available may not have a mechanical structure capable of mounting the drive so that it can meet the shock and vibration specifications listed in this manual.

8.6 Grounding

Signal ground (PCBA) and HDA ground are connected together in the drive and cannot be separated by the user. The equipment in which the drive is mounted is connected directly to the HDA and PCBA with no electrically isolating shock mounts. If it is desired for the system chassis to not be connected to the HDA/PCBA ground, the systems integrator or user must provide a nonconductive (electrically isolating) method of mounting the drive in the host equipment.

Increased radiated emissions may result if you do not provide the maximum surface area ground connection between system ground and drive ground. This is the system designer's and integrator's responsibility.

9.0 Interface requirements

This section partially describes the interface requirements as implemented on ST150176FC drives. Additional information is provided in the *Fibre Channel Interface Manual* (part number 77767496).

9.1 FC-AL features

This section lists the Fibre Channel-specific features supported by Barracuda 50FC drives.

9.1.1 Fibre Channel link service frames

Table 4 lists the link services supported by Barracuda 50FC drives.

Table 4: Link services supported

Type of frame	Link service
Basic link service frames	Abort Sequence (ABTS)
Basic link service reply frames	Basic_Accept (BA_ACC) Basic_Reject (BA_RJT)
Extended link service frames	N_Port Login (PLOGI) Fabric Login (FLOGI) Logout (LOGO) Process Login (PRLI) Process Logout (PRLO) Read Link Status (RLS) Fabric Address Notification (FAN) Port Discovery (PDISC) Address Discovery (ADISC) Third-party Process Logout (TRPLO)
Extended link service reply frames	Accept (ACC) Link Service Reject (LS_RJT)
Fibre Channel Services	Register FC-4 Types (RFT_ID)

9.1.2 Fibre Channel task management functions

Table 5 lists the Fibre Channel SCSI Fibre Channel Protocol (FC SCSI FCP) task management functions supported.

Table 5: Fibre Channel SCSI FCP task management functions

Task name	Supported
Terminate task	No
Clear ACA	Yes
Target reset	Yes
Clear task set	Yes
Abort task set	Yes

9.1.3 Fibre Channel task management responses

Table 6 lists the FC SCSI FCP response codes returned for task management functions supported.

Table 6: FC SCSI FCP response codes

Function name	Response code
Function complete	00
Function not supported	04
Function reject	05

9.1.4 Fibre Channel port login

Table 7 identifies the required content of the N_Port Login (PLOGI) payload from an initiator.

Table 7: N_Port login (PLOGI) payload

Bytes																	
0-15	03	00	00	00	09	09	BB	BB	CF	XX	FS	FS	XX	XX	XX	XX	Common
16-31	XX	XX	XX	XX	PN	PN	PN	PN	PN	PN	PN	PN	NN	NN	NN	NN	
32-35	NN	NN	NN	NN													
36-47					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Class 1
48-51	XX	XX	XX	XX													
52-63					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Class 2
64-67	XX	XX	XX	XX													
68-79					SO	SO	IC	IC	XX	XX	FS	FS	XX	CS	XX	XX	Class 3
80-83	OS	OS	XX	XX													
84-95					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Reserved
96-99	XX	XX	XX	XX													
100-111					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Vendor
112-115	XX	XX	XX	XX													Version

- X Indicates a four-bit (hex) field is not checked.
- x Indicates a single bit is not checked.
- BB BB-Credit. This field is not checked. The FC-AL drive uses BB-Credit of zero (0).
- CF Common features. This binary field selects the common features requested by the initiator login.
 - MSB Continuously increasing offset Must = 1
 - Random relative offset Not checked. Port Login Accept will return a 0—not supported.
 - Valid version level x
 - N_Port/F_Port Must = 0, N_Port
 - Alternate credit model Must = 1
 - Other bits reserved xxx XX
- FS Receive buffer field size. The FS field in the common and Class 3 parameters is checked for the range 128 < FS < 2,112 and a multiple of four bytes. For multiple frame sequences, all frames but the last frame of the sequence must be this size. Only the receive buffer field size in the Class 3 parameters is used.
- PN Port name (initiator's)—saved with the login parameters. If a change of the port name/AL_PA address association is detected during a Port DISCOVERY, and implicit logout occurs and the initiator returns a LS_RJT.
- NN Node name. The node name is not checked or saved by the drive.
- SO Service options Class 3 only.
 - MSB Class valid Must = 1
 - Intermix x
 - Stacked connection req. xx
 - Sequential delivery x
 - Other bits reserved xxx XX
- IC Initiator control
 - MSB XID reassign xx
 - Proc Assc 10 or 11 causes the login to be rejected. Other values are accepted.
 - Other bits XXX

- CS Concurrent sequences Must be a value greater than 0.
- OS Open sequences per exchange Must be a value greater than 0.

9.1.5 Fibre Channel port login accept

Table 8 identifies the N_Port Login access payload values.

Table 8: N_Port Login Accept (ACC) payload

Bytes																	
0-15	02	00	00	00	09	09	00	00	88	00	FS	FS	00	FF	00	01	Common
16-31	00	00	01	F4	20	PP	00	20	37	UI	UI	UI	20	00	00	20	
32-35	37	UI	UI	UI													
36-47					00	00	00	00	00	00	00	00	00	00	00	00	Class 1
48-51	00	00	00	00													
52-63					00	00	00	00	00	00	00	00	00	00	00	00	Class 2
64-67	00	00	00	00													
68-79					80	00	00	00	00	00	FS	FS	00	FF	00	00	Class 3
80-83	00	01	00	00													
84-95					00	00	00	00	00	00	00	00	00	00	00	00	Reserved
96-99	00	00	00	00													
100-111					00	00	00	00	00	00	00	00	00	00	00	00	Vendor -
112-115	00	00	00	00													Version

- FS Receive buffer field size. The drive returns and uses the receive buffer size from the N_Port Login Class 3 receive buffer.
- UI Unique identifier. This 24-bit field is uniquely assigned to the drive. This same UI appears in the Port Name and Node Name fields.
- PP Port identifier field.
 - 01 P_LOGI received on Port A.
 - 02 P_LOGI received on Port B.

9.1.6 Fibre Channel Process Login

Table 9 lists the process login payload data.

Table 9: Process Login (PLRI) payload

Bytes																
0-15	20	10	00	14	08	00	20	00	XX	XX	XX	XX	XX	XX	XX	XX
16-19	00	00	00	22												

XX Indicates fields that are not used.

9.1.7 Fibre Channel Process Login Accept

Table 10 lists Barracuda 50FC process login accept payload data.

Table 10: Process Login Accept (ACC) payload

Bytes																
0-15	02	10	00	14	08	00	21	00	00	00	00	00	00	00	00	00
16-31	00	00	00	12												

9.1.8 Fibre Channel fabric login

Table 11 lists the fabric login payload from the drive.

Table 11: Fabric Login (FLOGI) payload

Bytes																	
0-15	04	00	00	00	09	09	00	00	08	00	08	40	00	00	00	00	Common
16-31	00	00	02	F4	20	PP	00	20	37	UI	UI	UI	02	00	00	20	
32-35	37	UI	UI	UI													
36-47					00	00	00	00	00	00	00	00	00	00	00	00	Class 1
48-51	00	00	00	00													
52-63					00	00	00	00	00	00	00	00	00	00	00	00	Class 2
64-67	00	00	00	00													
68-79					80	00	00	00	00	00	08	40	00	00	00	00	Class 3
80-83	00	00	00	00													
84-95					00	00	00	00	00	00	00	00	00	00	00	00	Reserved
96-99	00	00	00	00													
100-111					00	00	00	00	00	00	00	00	00	00	00	00	Vendor -
112-115	00	00	00	00													Version

UI Unique identifier. This 24-bit field is uniquely assigned to the drive. This same UI appears in the Port Name and Node Name fields.

PP Port identifier field.

01 FLOGI originated on Port A.

02 FLOGI originated on Port B.

9.1.9 Fibre Channel fabric accept login

Table 11 lists the required content of the Fabric Login Accept (ACC) payload from the fabric.

Table 12: Fabric Login Accept (ACC) payload

Bytes																	
0-15	02	00	00	00	09	09	BB	BB	CF	XX	FS	FS	R_	A_	T0	V_	Common
16-31	E_	D_	T0	V_	PN	PN	PN	PN	PN	PN	PN	PN	NN	NN	NN	NN	
32-35	NN	NN	NN	NN													
36-47					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Class 1
48-51	XX	XX	XX	XX													
52-63					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Class 2
64-67	XX	XX	XX	XX													
68-79					SO	SO	xx	xx	XX	XX	FS	FS	XX	xx	XX	XX	Class 3
80-83	OS	OS	XX	XX													
84-95					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Reserved
96-99	XX	XX	XX	XX													
100-111					XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	Vendor -
112-115	XX	XX	XX	XX													Version

X Indicates a four-bit (hex) field is not checked.

x Indicates a single bit is not checked.

BB BB-Credit. This field is not checked. The FC-AL drive uses BB-Credit of zero (0).

CF Common features. This binary field selects the common features requested by the fabric login.

MSB	Continuously increasing offset	x
	Random relative offset	x
	Valid version level	x
	N_Port/F_Port	Must = 1, F_Port
	Alternate credit model	Must = 1
	Other bits reserved	xxx XX

FS Receive buffer field size. The FS field in the common and Class 3 parameters is checked for the range $128 < FS < 2,112$ and a multiple of four bytes. The receive buffer field size in the Class 3 parameters is used. The drive uses the lower FS of Fabric Login Accept or N_Port Login when sending frames to an initiator.

PN Port Name. The fabric port name is saved with the login parameters. If a change of the port name is detected during a FAN, an implicit logout occurs and a LS_RJT is returned to the fabric.

NN Node Name. The drive does not check or save the node name.

SO Service Options—Class 3 only.

MSB	Class valid	Must = 1
	Intermix	x
	Stacked connection req.	xx
	Sequential delivery	Must = 1
	Other bits reserved	xxx XX

9.1.10 Fibre Channel Arbitrated Loop options

Table 13 lists the FC-AL options supported by Barracuda 50FC drives.

Table 13: FC-AL options supported

Option	Supported
OPEN Half Duplex	Accepted from another device.
OPEN Full Duplex	Sent to open another device. Accepted from another device.
Private Loop	Yes
Public Loop	Yes
Old Port State	No
Loop Position	Yes
Loop Position Report	Yes

9.2 Dual port support

ST150176FC drives have two independent FC-AL ports. These ports may be connected on independent loops or on the same loop. Port A and Port B may be connected in any order or combination.

- If both ports are connected on independent loops and hard addressing is used, the drive interface address is selected through the interface connector, both ports will seek the same loop address. If no conflict, both ports will have the same loop address.
- If both ports are connected in the same loop and hard addressing is used, at least one port will attempt taking a soft address to prevent an address conflict.

Note. When a Barracuda 50FC drive is connected in loops with previous Seagate FC drive products:

Barracuda 4LP FC (ST32171FC, ST34371FC, and ST34571FC)
 Barracuda 9FC (ST19171FC)
 Cheetah 4LP FC (ST34501FC)
 Cheetah 9FC (ST19101FC)

the connection of Port A and B for these products must follow the requirements in their product manuals.

Subject to buffer availability, the Barracuda 50FC drives support:

- Concurrent port transfers—The drive supports receiving transfers on both ports at the same time when the ports are on independent loops.
- Full duplex—The drive supports sending FCP_Data, FCP_RSP, FCP_XFR_RDY and ELS transfers while receiving frames on both ports.

9.3 SCSI commands supported

Table 14 lists the SCSI commands that ST150176FC drives support.

Table 14: Supported commands

Command code	Supported	Command name
00h	Y	Test unit ready
01h	Y	Rezero unit
03h	Y	Request sense
	Y	Extended sense
	Y	Field pointer bytes
	Y	Actual retry count bytes
04h	Y	Format unit [9.3.1]
	N	Block format
	Y	Bytes from Index
	Y	Physical sector format
	Y	DPRY bit supported
	Y	DCRT bit supported
	Y	STPF bit supported
	Y	IP bit supported
	Y	DSP bit supported
	Y	IMMED bit supported
	N	VS (vendor specific)
07h	Y	Reassign blocks
08h	Y	Read
0Ah	Y	Write
0Bh	Y	Seek
12h	Y	Inquiry
	Y	Vital product data page (00h)
	Y	Unit serial number page (80h)
	Y	Implemented operating def. page (81h)
	Y	Firmware numbers page (C0h)
	Y	Date code page (C1h)
	Y	Jumper settings page (C2h)
	Y	Device behavior page (C3h)
15h	Y	Mode select (same pages as Mode Sense command shown below) [3]
16h	Y	Reserve
	Y	3rd party reserved
	N	Extent reservation
17h	Y	Release
18h	N	Copy
1Ah	Y	Mode sense
	Y	Unit attention page (00h)
	Y	Error recovery page (01h)

Table 14: Supported commands (*continued*)

Command code	Supported	Command name
	Y	Disconnect/reconnect control (page 02h) DTDC and DIMM not used
	Y	Format page (03h)
	Y	Rigid disc drive geometry page (04h)
	Y	Verify error recovery page (07h)
	Y	Caching parameters page (08h)
	Y	Control mode page (0Ah)
	N	Notch and partition page (0Ch)
	N	Xor control page (10h)
	Y	Fibre Channel Interface control page (19h)
	Y	Power control page (1Ah)
	Y	Information exceptions control page (1Ch)
1Bh	Y	Start unit/stop unit (spindle ceases to rotate)
1Ch	Y	Receive diagnostic results
	Y	Supported diagnostics pages (00h)
	Y	Translate page (40h)
	Y	Enclosure services page
1Dh	Y	Send diagnostics page
	Y	Supported diagnostics page (00h)
	Y	Translate page (40h)
25h	Y	Read capacity
28h	Y	Read extended
	Y	Disable page out
	Y	Force unit access
	N	Relative address
2Ah	Y	Write extended
	Y	Disable page out
	Y	Force unit access
	N	Relative address
2Bh	Y	Seek extended
2Eh	Y	Write and verify
	Y	Disable page out
	Y	Byte check
	N	Relative address
2Fh	Y	Verify
	Y	Disable page out
	Y	Byte check
	N	Relative address
30h	N	Search data high
31h	N	Search data equal
32h	N	Search data low
33h	N	Set limits

Table 14: Supported commands (*continued*)

Command code	Supported	Command name
34h	N	Prefetch
35h	Y	Synchronize cache
36h	N	Lock-unlock-cache
37h	Y	Read defect data
39h	N	Compare
3Ah	N	Copy and verify
3Bh	Y	Write buffer
	Y	Write combined header and data mode (0)
	Y	Write data mode (2)
	N	Download microcode mode (4)
	Y	Download microcode and save modes (5)
	N	Download microcode with offsets mode (6)
	Y	Download microcode with offsets and save mode (7)
	Y	Firmware download option [2]
3Ch	Y	Read buffer
	Y	Read combined header and data mode (0)
	Y	Read data mode (2)
	Y	Read descriptor mode (3)
3Eh	Y	Read long
3Fh	Y	Write long
40h	Y	Change definition
41h	Y	Write same
	N	PBdata
	N	LBdata
42-4Bh	N	Not used
4Ch	Y	Log Select
	N	DU bit
	Y	DS bit
	Y	TSD bit
	N	ETC bit
	N	TMC bit
	N	LP bit
4Dh	Y	Log Sense
	Y	Cache statistics page (37h)
	Y	Non-medium error page (06h)
	Y	Pages supported list (00h)
	Y	Power-on time page (3Eh)
	Y	Read error counter page (03h)
	Y	S.M.A.R.T. status log page (2Fh)
	Y	S.M.A.R.T. attribute log page (30h)
	Y	Verify error counter page (05h)

Table 14: Supported commands (*continued*)

Command code	Supported	Command name
	Y	Write error counter page (02h)
4E-4Fh	N	Not used
50h	N	XD write
51h	N	XP write
52h	N	XD read
53-54h	N	Not used
55h	Y	Mode Select (10) [3]
	Y	Capacity programming
56h	Y	Reserved (10)
	Y	3rd party reserve
	N	Extent reservation
57h	Y	Released (10)
58-59h	N	Not used
5Ah	Y	Mode Sense (10) [3]
5B-5Dh	N	Not used
5E	Y	Persistent reserve in
5F	Y	Persistent reserve out
60-7Fh	N	Not used
80h	N	XD write extended
81h	N	Rebuild
82h	N	Regenerate
83-8Fh	N	Not used
C0-DFh	N	Not used
EO-FFh	N	Not used

- [1] Barracuda 50FC drives can format to any multiple of four bytes per logical block in the range 512 to 4,096 bytes.
- [2] **Warning.** Power loss during flash programming can result in firmware corruption. This usually makes the drive inoperable.
- [3] Reference Mode Sense command 1Ah for mode pages supported.

9.3.1 Inquiry data

Table 15 lists the Inquiry command data that the drive should return to the initiator per the format given in the *Fibre Channel Interface Manual*.

Table 15: Barracuda 50FC inquiry data (ST150176FC data shown)

Bytes	Data (hex)																
0-15	00	00	02*	32	8B	00	PP	0A	53	45	41	47	41	54	45	20	Vendor ID
16-31	[53	54	31	35	30	31	37	36	46	43]	20	20	20	20	20	20	Product ID
32-47	R#	R#	R#	R#	S#	S#	S#	S#	S#	S#	S#	S#	00	00	00	00	
48-63	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
64-79	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
80-95	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
96-111	00	43	6F	70	79	72	69	67	68	74	20	28	63	29	20	31*	*Copy-right
112-127	39*	39*	39*	20	53	65	61	67	61	74	65	20	41	6C	6C	20	notice
128-143	72	69	67	68	74	73	20	72	65	73	65	72	76	65	64	20	

* Copyright year (changes with actual year).

** 02 = SCSI-2 implemented with some SCSI-3 features (default).

PP 50 = Inquiry data for an Inquiry command received on Port A.

70 = Inquiry data for an Inquiry command received on Port B.

R# Four ASCII digits representing the last four digits of the product firmware version number.

S# Eight ASCII digits representing the eight digits of the product serial number.

[] Bytes 16 through 25 reflect drive model (ST150176FC shown).

9.3.2 Mode Sense data

The Mode Sense command provides a way for the drive to report its operating parameters to the initiator. The drive maintains four sets of mode parameters:

1. Default values

Default values are hard-coded in the drive firmware stored in flash E-PROM (nonvolatile memory) on the drive's PCB. These default values can be changed only by downloading a complete set of new firmware into the flash E-PROM. An initiator can request and receive from the drive a list of default values and use those in a Mode Select command to set up new current and saved values, where the values are changeable.

2. Saved values

Saved values are stored on the drive's media using a Mode Select command. Only parameter values that are allowed to be changed can be changed by this method. Parameters in the saved values list that are not changeable by the Mode Select command get their values from default values storage.

When power is applied to the drive, it takes saved values from the media and stores them as current values in volatile memory. It is not possible to change the current values (or the saved values) with a Mode Select command before the drive achieves operating speed and is "ready." An attempt to do so results in a "Check Condition" status.

On drives requiring unique saved values, the required unique saved values are stored into the saved values storage location on the media prior to shipping the drive. Some drives may have unique firmware with unique default values also.

On standard OEM drives, the saved values are taken from the default values list and stored into the saved values storage location on the media prior to shipping.

3. Current values

Current values are volatile values being used by the drive to control its operation. A Mode Select command can be used to change the values identified as changeable values. Originally, current values are installed from saved or default values after a power on reset, hard reset, or Bus Device Reset message.

4. Changeable values

Changeable values form a bit mask, stored in nonvolatile memory, that dictates which of the current values and saved values can be changed by a Mode Select command. A one (1) indicates the value can be changed. A zero (0) indicates the value is not changeable. For example, in Table 16, refer to Mode page 81, in the row entitled "CHG." These are hex numbers representing the changeable values for Mode page 81. Note in columns 5 and 6 (bytes 04 and 05), there is 00h which indicates that in bytes 04 and 05 none of the bits are changeable. Note also that bytes 06, 07, and 09 are not changeable, because those fields are all zeros. In byte 02, hex value FF equates to the binary pattern 11111111. If there is a zero in any bit position in the field, it means that bit is not changeable. Since all of the bits in byte 02 are ones, all of these bits are changeable.

The changeable values list can only be changed by downloading new firmware into the flash E-PROM.

Note. Because there are often several different versions of drive control firmware in the total population of drives in the field, the Mode Sense values given in the following tables may not exactly match those of some drives.

The following tables list the values of the data bytes returned by the drive in response to the Mode Sense command pages for SCSI implementation (see the *Fibre Channel Interface Manual*).

Definitions:

DEF = Default value. Standard OEM drives are shipped configured this way.

CHG = Changeable bits; indicates if default value is changeable.

9.4 Miscellaneous operating features and conditions

Table 17 lists various features and conditions. A “Y” in the support column indicates the feature or condition is supported. An “N” in the support column indicates the feature or condition is not supported.

Table 17: Miscellaneous features

Supported	Feature or condition
Y	FC-AL selective reset
Y	Automatic contingent allegiance
N	Asynchronous event notification
N	Synchronized (locked) spindle operation
Y	Segmented caching
N	Zero latency read
Y	Queue tagging (up to 128 queue tags supported)
Y	Deferred error handling
Y	Parameter rounding (controlled by Round bit in Mode Select page 0)
Y	Reporting actual retry count in Extended Sense bytes 15, 16, and 17
N	Adaptive caching
Y	SMP = 1 in Mode Select command needed to save RPL and rotational offset bytes

Table 18: Miscellaneous status

Supported	Status
Y	Good
Y	Check condition
Y	Condition met/good
Y	Busy
Y	Intermediate/good
Y	Intermediate/condition met/good
Y	Reservation conflict
Y	Task set full
Y	ACA active
Y	ACA active, faulted initiator

9.5 FC-AL physical interface

Figure 12 shows the location of the J1 Fibre Channel single connection attachment (FC-SCA) and the J6 connector. Figure 14 provides the dimensions of the FC-SCA. Figure 15 provides the dimensions of the J6 connector.

Details of the physical, electrical, and logical characteristics are provided within this section. The operational aspects of Seagate's Fibre Channel drives are provided in the *Fibre Channel Interface Manual*.

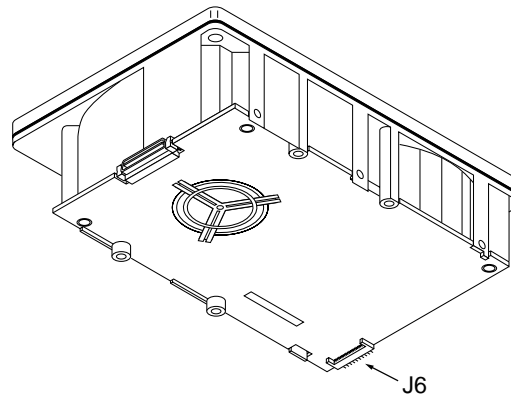


Figure 12. Physical interface

9.5.1 Physical characteristics

This section defines physical interface connector.

9.5.1.1 Physical description

Fibre Channel drives may be connected in a loop together or with other compatible FC-AL devices. A maximum of 127 devices may have addresses; however, one of the addresses is reserved for a fabric port switch device. This means 126 addresses are available for FC-AL devices. More FC-AL compatible devices may physically reside on the loop, but they will not be functional because they would not be able to obtain valid addresses.

Port bypass circuits (PBCs) allow devices to be inserted into unpopulated locations or removed from the loop with loop operation recovery after a brief interruption. These PBCs are located external to the FC-AL device. Figure 13 shows the relationship between the PBC and FC-AL device.

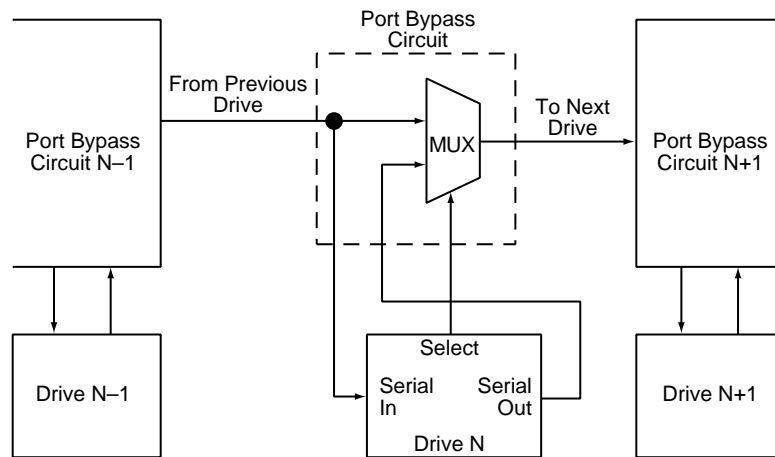


Figure 13. Port bypass circuit physical interconnect

9.5.2 Connector requirements

Recommended mating SCA part number:

Part description	Positions	Part number	Features
AMP Vertical (SCA sequence)	40	787317-1	With polarization
Berg	40	71781	With polarization
Methode	40	512-220-91-101N	With polarization
Molex	40	717431040	With polarization

The FC-AL SCA device connector is illustrated in Figure 14. The J6 connector is illustrated in Figure 15.

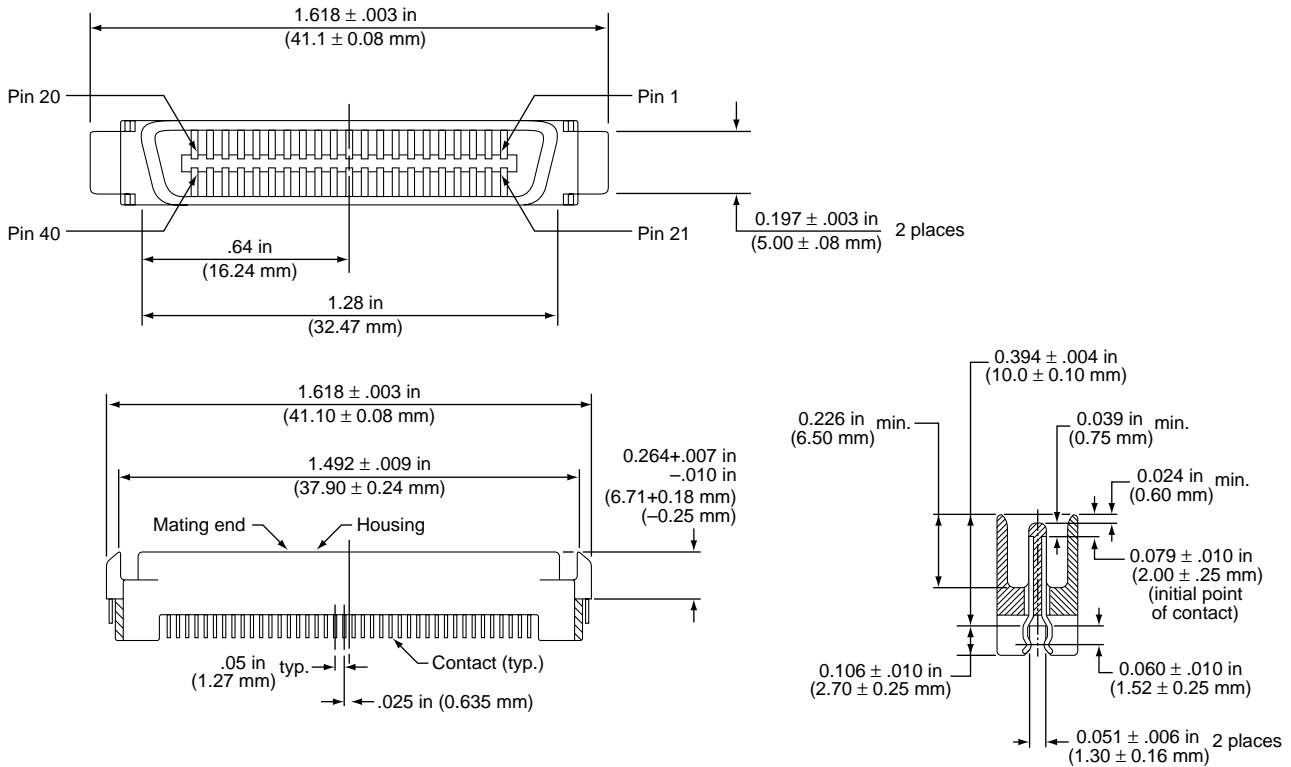


Figure 14. FC-AL SCA device connector dimensions

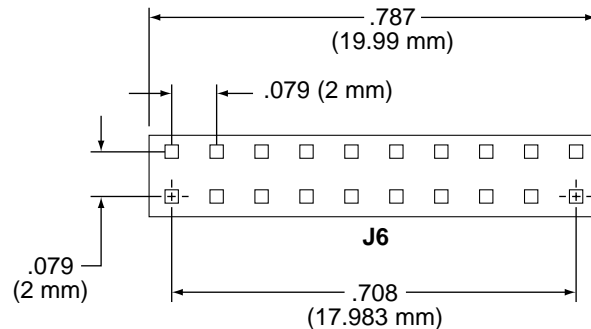


Figure 15. J6 connector dimensions

9.5.3 Electrical description

Fibre Channel drives use the FC-SCA connector for:

- DC power
- FC-AL interface
- Drive select (device identification)
- Option selection
- Enclosure Services interface

This 40-pin connector is designed to plug directly into a backpanel. External cables are not required.

9.5.4 Pin descriptions

This section provides a pin-out of the FC-SCA and a description of the functions provided by the pins.

Table 19: FC-SCA pin descriptions

Pin	Signal name	Signal type	Pin	Signal name	Signal type
1*	-EN bypass port A	TTL output	21	12 Volts charge	
2*	12 Volts		22	Ground	
3*	12 Volts		23	Ground	
4*	12 Volts		24*	+Port A_in	Diff. PECL input pair
5*	-Parallel ESI		25*	-Port A_in	
6*	Ground ^[9.3.1]		26	Ground	
7*	Active LED out	Open collector out	27*	+Port B_in	Diff. PECL input pair
8*	Reserved		28*	-Port B_in	
9*	Start_1	TTL input	29	Ground	
10*	Start_2	TTL input	30*	+Port A_out	Diff PECL output pair
11*	-EN bypass port B	TTL output	31*	-Port A_out	
12*	SEL_6	TTL input/output	32	Ground	
13*	SEL_5	TTL input/output	33*	+Port B_out	Diff PECL output pair
14*	SEL_4	TTL input	34*	-Port B_out	
15*	SEL_3	TTL input/output	35	Ground	
16*	Fault LED out	Open collector out	36	SEL_2	TTL input/output
17*	Reserved NC		37	SEL_1	TTL input/output
18*	Reserved NC		38	SEL_0	TTL input/output
19*	5 Volts		39	Reserved NC	
20*	5 Volts		40	5 Volts charge	

*Short pins in mating backpanel connector.

- [1] This pin may be connected to external logic to detect the presence of the drive. The drive connects this pin to the common ground.

9.5.5 FC-AL transmitters and receivers

A typical FC-AL differential copper transmitter and receiver pair is shown in Figure 16. The receiver is required to provide the AC coupling to eliminate ground shift noise.

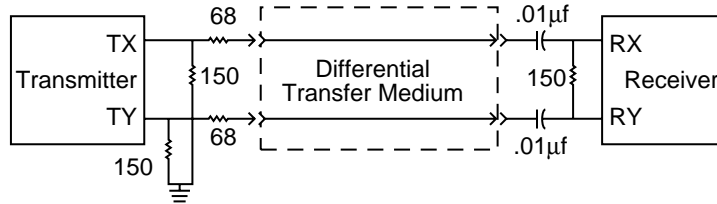


Figure 16. FC-AL transmitters and receivers

9.5.6 Power

Power is supplied through the FC-SCA with support for +5 volts and +12 volts. All of the voltage pins in the drive connector are the same length.

Four 12 volt pins provide +12 volt power to the drive. The current return for the +12 volt power supply is through the common ground pins. The supply current and return current must be distributed as evenly as possible among the pins. The maximum current typically occurs while the drive motor is starting.

Three 5 volt pins provide logic power to the drive. The current return for the +5 volt power supply is through the common ground pins. Distribute supply and return current as evenly as possible among the voltage and ground pins.

The mating connector pins use shorter contacts to achieve power surge reductions and to aid in "hot plugging" the drives. There are longer voltage contacts in the connector to enable the drive filter capacitors to charge. Current to the drive through the long charge pins is limited by the system in which the drive operates. Three of the +12 volt pins are shorter to allow capacitive pre-charging through the longer +12 volt charge pin. Two of the +5 volt pins are shorter to allow capacitive precharging through the longer +5 volt charge pin.

9.5.7 Fault LED Out

The Fault LED Out signal is driven by the drive when:

- the drive detects failure of both ports
- the drive detects an internal failure
- the drive receives the appropriate fault LED command from the host

The Fault LED Out signal is designed to pull down the cathode of an LED. The anode is attached to the proper +5 volt supply through an appropriate current-limiting resistor. The LED and the current-limiting resistor are external to the drive.

9.5.8 Active LED Out

The Active LED Out signal is driven by the drive as indicated in Table 20.

Table 20: Active LED Out conditions

Normal command activity	LED status
Spun down and no activity	Slow blink (20% on and 80% off a 2 sec cycle)
Spun down and activity (command executing)	On
Spun up and no activity	On
Spun up and activity (command executing)	Off
Spinning up or down	Blinks steadily (50% on and 50% off)
Format in progress, each cylinder change	Toggles on/off

The Active LED Out signal is designed to pull down the cathode of an LED. The anode is attached to the proper +5 volt supply through an appropriate current limiting resistor. The LED and the current limiting resistor are external to the drive.

9.5.9 Enable port bypass signals

The – Enable Bypass Port A (– EN BYP Port A) and – Enable Bypass Port B (– EN BYP Port B) signals control the port bypass circuits (PBC) located external to the disc drive. The PBC allows a loop to remain functional in the event of a drive failure or removal. When these signals are active, low, the PBC bypasses the drive on the associated port. When an Enable Bypass signal is active, the corresponding Port Bypass LED signal in connector J1 is driven low by the disc drive. A pull down resistor, 1K, located with the PBC should be used to insure the bypass is enabled if the disc drive is not installed.

The Enable Bypass signal is active under failing conditions within the drive, on detection of the Loop Port Bypass primitive sequence, or on removal of the drive. In the bypass state the drive continues to receive on the inbound fibre. Enable Bypass may be deactivated by detection of a Loop Port Enable primitive sequence if the drive has completed self-test and a hardware failure is not present.

Failure modes detected by the disc drive that will enable bypass include:

- Transmitter/receiver wrap test failure
- Loss of receive clock
- Loss of transmission clock
- Drive interface hardware error

9.5.10 Motor start controls

The drive's motor is started according to the Start_1 and Start_2 signals described in Table 21. The state of these signals can be wired into the backplane socket or driven by logic on the backplane.

Table 21: Motor start control signals

Case	Start_2	Start_1	Motor spin function
1	Low	Low	Motor spins up at DC power on.
2	High	Low	Motor spins up only when SCSI Start command is received.
3	Low	High	Motor spins up after a delay of 12 seconds times the modulo 8 value of the numeric SEL ID of the drive from DC power on.
4	High	High	The drive will not spin up.

9.5.11 SEL_6 through SEL_0 ID lines

The SEL_6 through SEL_0 ID lines determine drive address, and, optionally, for an Enclosure Services Interface. When the Parallel ESI line is high, the enclosure backpanel must provide address information on the SEL line. Refer to table 22 for a mapping of SEL to FC-AL physical addresses (AL_PA). You can think of the SEL lines as the equivalent of a backpanel logic plug. The drives does not provide pull up resistors on these lines. The backpanel is required to provide high and low inputs to the SEL_ID lines per the specifications in table 23 on page 56.

Note. Table 22 gives AL_PA values for each SEL value. The first entry in the table is SEL_ID 00. The last entry is SEL_ID 7D. SEL_ID 7E is AL_PA 00 which is not valid for an NL_Port, so is not included in the table. Also, SEL_ID 7Fh does map to a valid AL_PA; however, this value signals the drive that physical addresses are not being assigned using the SEL lines and that a “soft” address will be determined by FC-AL loop initialization.

When the Parallel ESI line is low, the enclosure backpanel logic switches to ESI mode if supported. There are two modes of ESI, seven bits of enclosure status and a bidirectional mode. ESI support and the mode are determined by the drive using a discovery process. Refer to the *Fibre Channel Interface Manual* for a description of ESI operation.

9.5.11.1 Parallel Enclosure Services Interface (ESI)

The parallel ESI line is an output from the drive. This line provides the enclosure with an indication of the present function of the SEL lines. A high level, the default state, indicates the drive requires address information on the SEL lines. A low level indicates the drive is attempting an ESI transfer. The enclosure may not support ESI on any or all drive locations. It may only support the address function. Support of ESI is discovered by the drive. Refer to the *Fibre Channel Interface Manual* for a description of ESI operations.

Table 22: Arbitrated loop physical address (AL_PA) values

AL_PA (hex)	SEL ID (hex)	Setting (dec)	AL_PA (hex)	SEL ID (hex)	Setting (dec)	AL_PA (hex)	SEL ID (hex)	Setting (dec)
EF	00	00	A3	2B	43	4D	56	86
E8	01	01	9F	2C	44	4C	57	87
E4	02	02	9E	2D	45	4B	58	88
E2	03	03	9D	2E	46	4A	59	89
E1	04	04	9B	2F	47	49	5A	90
E0	05	05	98	30	48	47	5B	91
DC	06	06	97	31	49	46	5C	92
DA	07	07	90	32	50	45	5D	93
D9	08	08	8F	33	51	43	5E	94
D6	09	09	88	34	52	3c	5F	95
D5	0A	10	84	35	53	3A	60	96
D4	0B	11	82	36	54	39	61	97
D3	0C	12	81	37	55	36	62	98
D2	0D	13	80	38	56	35	63	99
D1	0E	14	7C	39	57	34	64	100
CE	0F	15	7A	3A	58	33	65	101
CD	10	16	79	3B	59	32	66	102
CC	11	17	76	3C	60	31	67	103
CB	12	18	75	3D	61	2E	68	104
CA	13	19	74	3E	62	2D	69	105
C9	14	20	73	3F	63	2C	6A	106
C7	15	21	72	40	64	2B	6B	107
C6	16	22	71	41	65	2A	6C	108
C5	17	23	6E	42	66	29	6D	109
C3	18	24	6D	43	67	27	6E	110
BC	19	25	6C	44	68	26	6F	111
BA	1A	26	6B	45	69	25	70	112
B9	1B	27	6A	46	70	23	71	113
B6	1C	28	69	47	71	1F	72	114
B5	1D	29	67	48	72	1E	73	115
B4	1E	30	66	49	73	1D	74	116
B3	1F	31	65	4A	74	1B	75	117
B2	20	32	63	4B	75	18	76	118
B1	21	33	5C	4C	76	17	77	119
AE	22	34	5A	4D	77	10	78	120
AD	23	35	59	4E	78	0F	79	121
AC	24	36	56	4F	79	08	7A	122
AB	25	37	55	50	80	04	7B	123
AA	26	38	54	51	81	02	7C	124
A9	27	39	53	52	82	01	7D	125
A7	28	40	52	53	83			

9.6 Signal characteristics

This section describes the electrical signal characteristics of the drive's input and output signals. See Table 19 on page 51 for signal type and signal name information.

9.6.1 TTL input characteristics

Table 23 provides the TTL characteristics.

Table 23: TTL characteristics

State	Voltage	Current
Input high	$1.9 < V_{IH} < 5.5V$	$I_{IH} = \pm 500nA$ max.
Input low	$-0.5V < V_{IL} < 0.9V$	$I_{OL} = \pm 500nA$ max.
Output high (-EN Bypass A, B)	$2.4 < V_{OH} < 5.25V$	$I_{OH} < -3mA$
Output low (-EN Bypass A, B)	$V_{OL} < 0.5V$	$I_{OL} < 3mA$
Output high (-Parallel ESI)	$2.4 V_{CC} < V_{OH}$ $V_{OH} > 0.9V_{CC}$	$I_{OH} < -2.4mA$ $I_{OH} < -500\mu A$
Output low (-Parallel ESI)	$0 < V_{OL} < .45V$	$I_{OL} < 2.4mA$
Output high (all other outputs)	$2.4 V_{CC} < V_{OH}$ $V_{OH} > 0.9V_{CC}$	$I_{OH} < -1.6mA$ $I_{OH} < -500\mu A$
Output low (all other outputs)	$0 < V_{OL} < .45V$	$I_{OL} < 1.6mA$

9.6.2 LED driver signals

Fault and Active LED signals are located in the FC-SCA connector (J1) and through the indicator connector (J6). See Table 24 for the output characteristics of the LED drive signals.

Table 24: LED drive signal

State	Current drive available	Output voltage
LED off, high	$0 < I_{OH} < 100\mu A$	
LED on, low	$I_{OL} < -30 mA$	$0 < V_{OL} < 0.8V$

9.6.3 Differential PECL output

The serial PECL output signal voltage characteristics are provided in Table 25. The outputs are not AC coupled in order to deliver maximum signal without rise and fall time degradation. You must AC couple the receiver to isolate potentially different DC characteristics of the outputs and the receiver.

Table 25: Differential PECL output characteristics

Description	Parameter	Notes
Serial output voltage swing	$300 < V_{out} < 650 mV$	Centered at 1.32V

Figure 17 provides the data output valid eye diagram relative to the bit cell time.

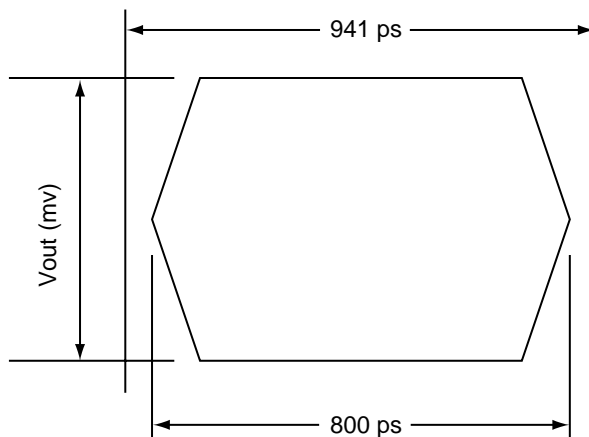


Figure 17. Transmit eye diagram

9.6.4 Differential PECL input

The serial PECL input signal voltage characteristics are provided in Table 26.

Table 26: Differential PECL input characteristics

Description	Parameter	Notes
Serial input voltage swing	$200\text{mV} < V_{in} < 1300\text{ mV}$	AC coupled

Figure 18 provides the data valid eye diagram for typical and minimum requirements to recover data at the specified interface error rate. The inputs are AC coupled on the drive.

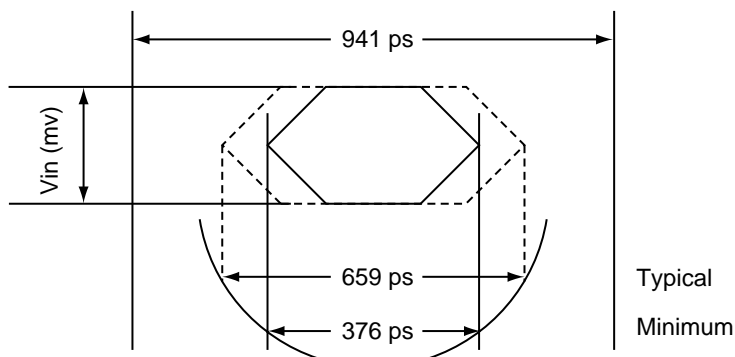


Figure 18. Receive eye diagram

10.0 Seagate Technology support services

Online Services

Internet

For online information about Seagate products, visit www.seagate.com or e-mail your disc or tape questions to:

Presales Support:

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Tape_Sales_Support@Seagate.com

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If you need help installing your drive, consult your dealer. Dealers are familiar with their unique system configurations and can help you with system conflicts and other technical issues. If you need additional help, you can talk to a Seagate technical support specialist. Before calling, note your system configuration and drive model number (ST####).

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